



STIC Search Report

EIC 2600

STIC Database Tracking Number: 175644

TO: Ryan Yang
Location: KNX10A19
Art Unit : 2672
Friday, January 06, 2006

Case Serial Number: 10/661055

From: Samir Patel
Location: EIC 2600
KNX-8B68
Phone: 571-272-3537

Samir.patel@uspto.gov

Search Notes

Dear Examiner,

Attached are the search results (from commercial databases) for your case.

Tags mark the patent/articles, which might be of interest. After you review all records including tagged and untagged records, if you wish to order the complete text of any record, please submit request(s) directly to the STIC-EIC 2600 Email Box.

Please call if you have any questions or suggestions, and I have enclosed a Search Results Feedback Form to facilitate further comments or suggestions.

Thanks

Samir Patel



By Friday

6

RUSH SPE SIGNATURE _____

Access DB#

175644

SEARCH REQUEST FORM
Scientific and Technical Information Center

EIC 2600

Requester's Full Name Ryan Yang Examiner # 76963 Date 1/4/06
Art Unit 2672 Phone Number 2-7666 Serial Number 10/661,055
Office Location KNX/0A/19 Format preferred (circle) PAPER EMAIL BOTH

If more than one search is submitted, please prioritize searches in order of need.
.....

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Let us know what you already have and so do not need. Include the keywords, synonyms and meaning of acronyms. Define all terms that may have a specific meaning. Please attach a copy of the background, abstract, claims and other pertinent information.

Please state how the terms or keyword strings should relate to one another.

Title of the Invention _____
Inventor(s) _____

Earliest Priority date to be used 9/13/03

using variable length fixed-point format to do rendering

"variable length" with "fixed-point" and rendering

(already searched EAST)

1111 1111

9:00 A.M to 5:30 p.M -everyday

STAFF USE ONLY

Searcher Sami S Patel

Phone 2-3537

Location KNX-8868

Date picked up 01/05/06/3:00p.M

Date completed 01/06/06/11:00a.M

Search Prep/review 120

Online Time 166

TYPE of Search

Text ☒

Litigation ☐

Other ☐

Databases Searched

Dialog ☒

STN ☐

QuestelOrbit ☐

LEXIS/NEXIS ☐

Courtlink ☐

Other ☒

File 9:Business & Industry(R) Jul/1994-2006/Jan 06
(c) 2006 The Gale Group
File 15:ABI/Inform(R) 1971-2006/Jan 05
(c) 2006 ProQuest Info&Learning
File 16:Gale Group PROMT(R) 1990-2006/Jan 06
(c) 2006 The Gale Group
File 20:Dialog Global Reporter 1997-2006/Jan 06
(c) 2006 Dialog
File 47:Gale Group Magazine DB(TM) 1959-2006/Jan 06
(c) 2006 The Gale group
File 75:TGG Management Contents(R) 86-2006/Dec W4
(c) 2006 The Gale Group
File 80:TGG Aerospace/Def.Mkts(R) 1982-2006/Jan 06
(c) 2006 The Gale Group
File 88:Gale Group Business A.R.T.S. 1976-2006/Jan 04
(c) 2006 The Gale Group
File 98:General Sci Abs/Full-Text 1984-2004/Dec
(c) 2005 The HW Wilson Co.
File 112:UBM Industry News 1998-2004/Jan 27
(c) 2004 United Business Media
File 141:Readers Guide 1983-2004/Dec
(c) 2005 The HW Wilson Co
File 148:Gale Group Trade & Industry DB 1976-2006/Jan 06
(c)2006 The Gale Group
File 160:Gale Group PROMT(R) 1972-1989
(c) 1999 The Gale Group
File 275:Gale Group Computer DB(TM) 1983-2006/Jan 06
(c) 2006 The Gale Group
File 264:DIALOG Defense Newsletters 1989-2006/Jan 05
(c) 2006 Dialog
File 369:New Scientist 1994-2005/Aug W2
(c) 2005 Reed Business Information Ltd.
File 370:Science 1996-1999/Jul W3
(c) 1999 AAAS
File 484:Periodical Abs Plustext 1986-2006/Jan W1
(c) 2006 ProQuest
File 553:Wilson Bus. Abs. FullText 1982-2004/Dec
(c) 2005 The HW Wilson Co
File 570:Gale Group MARS(R) 1984-2006/Jan 06
(c) 2006 The Gale Group
File 608:KR/T Bus.News. 1992-2006/Jan 06
(c)2006 Knight Ridder/Tribune Bus News
File 620:EIU:Viewswire 2005/Oct 19
(c) 2005 Economist Intelligence Unit
File 613:PR Newswire 1999-2006/Jan 06
(c) 2006 PR Newswire Association Inc
File 621:Gale Group New Prod.Annou.(R) 1985-2006/Jan 06
(c) 2006 The Gale Group
File 623:Business Week 1985-2005/Dec 22
(c) 2005 The McGraw-Hill Companies Inc
File 624:McGraw-Hill Publications 1985-2006/Jan 06
(c) 2006 McGraw-Hill Co. Inc
File 634:San Jose Mercury Jun 1985-2006/Jan 05
(c) 2006 San Jose Mercury News
File 635:Business Dateline(R) 1985-2006/Jan 05
(c) 2006 ProQuest Info&Learning
File 636:Gale Group Newsletter DB(TM) 1987-2006/Jan 05
(c) 2006 The Gale Group
File 647:CMP Computer Fulltext 1988-2006/Jan W2
(c) 2006 CMP Media, LLC
File 696:DIALOG Telecom. Newsletters 1995-2006/Jan 05

(c) 2006 Dialog
 File 674:Computer News Fulltext 1989-2005/Oct W2
 (c) 2005 IDG Communications
 File 810:Business Wire 1986-1999/Feb 28
 (c) 1999 Business Wire
 File 813:PR Newswire 1987-1999/Apr 30
 (c) 1999 PR Newswire Association Inc
 File 587:Jane`s Defense&Aerospace 2005/Dec W3
 (c) 2005 Jane`s Information Group

Set	Items	Description
S1	24662	(VARIABLE??? OR ADJUST????)(2N)(LENGTH?? OR DIGIT??)
S2	26650	FIX??(2N)POINT??
S3	8552757	IMAG?? OR GRAPHIC???? OR PICTURE??
S4	1223621	(CONVERT????? OR CONVER??????? OR TRANSFORM????? OR CHANG?- ??? OR TRANSLAT????)(7N)(FORMAT?? OR DESIGN?? OR STRUCTU???? - OR ARRANGE????? OR PATTERN???)
S5	7753	DIRECT3D OR DIRECT???()3D??
S6	2075	AU=(WANG L? OR WANG, L? OR DENG K? OR DENG, K? OR GUO B? OR GUO, B? OR BUCKMAN J? OR BUCKMAN, J?)
S7	382487	(RENDER????? OR PROCESS????)(5N)S3
S8	0	S1(S)S2(S)S4(S)S5(S)S7
S9	0	S1(S)S2(S)S4(S)S7
S10	0	S1(S)S2(S)S7
S11	0	S1(S)S2(S)S3
S12	23	S1(S)S2
S13	14	RD (unique items)
S14	13	S13 NOT PY>2003
S15	0	S6 AND S1 AND S2

14/3,K/1 (Item 1 from file: 9)
DIALOG(R)File 9:Business & Industry(R)
(c) 2006 The Gale Group. All rts. reserv.

02333163 Supplier Number: 25935353 (USE FORMAT 7 OR 9 FOR FULLTEXT)
Robot replication
(Brandeis University develops an experimental robot that designed and
constructed another robot; Hayden Switch & Instrument's 20 mm Z-Series
bi-polar motor was used)
Design News, v 55, n 24, p 35
December 18, 2000
DOCUMENT TYPE: Journal ISSN: 0011-9407 (United States)
LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 189

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:
...simulated and designed truss structures. "Trusses are well understood
engineering structures," says Lipson. "We replace **fixed points** with
ball joints and added **variable length** motors driven by artificial
neurons to act as the muscles and brains."

The motor used...

14/3,K/2 (Item 2 from file: 9)
DIALOG(R)File 9:Business & Industry(R)
(c) 2006 The Gale Group. All rts. reserv.

00590006 Supplier Number: 23112049 (USE FORMAT 7 OR 9 FOR FULLTEXT)
Two 'Lode' up on TCSI's new DSP core
(TCSI signs Anam Semiconductor & Technology and Japanese manufacturer for
low-power DSP engine, or Lode; Clarkspur adds 3 licensees)
Electronic Engineering Times, n 831, p 12
January 16, 1995
DOCUMENT TYPE: Journal ISSN: 0192-1541 (United States)
LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 563

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:
...Two unnamed U.S. semiconductor manufacturers and Sanyo Electric Co. Ltd.
have acquired the latest **adjustable -bit- length** (16-to-24-bit **fixed -**
point) 20-ns core, the CD2450.

Clarkspur will have a gate-array version of its core...

14/3,K/3 (Item 1 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2006 The Gale Group. All rts. reserv.

08247371 Supplier Number: 69391892 (USE FORMAT 7 FOR FULLTEXT)
Robot replication.
Wiebusch, Bruce
Design News, v55, n24, p35
Dec 18, 2000
Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Refereed; Academic Trade
Word Count: 187

... simulated and designed truss structures. "Trusses are well understood engineering structures," says Lipson. "We replace **fixed points** with ball joints and added **variable length** motors driven by artificial neurons to act as the muscles and brains."
The motor used...

14/3,K/4 (Item 2 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2006 The Gale Group. All rts. reserv.

07996804 Supplier Number: 63718274 (USE FORMAT 7 FOR FULLTEXT)
Advanced VLIW Architectures Unleash Raw DSP Horsepower.(Product Information)
Bindra, Ashok
Electronic Design, v48, n10, p73
May 15, 2000
Language: English Record Type: Fulltext Abstract
Document Type: Magazine/Journal; Trade
Word Count: 3721

... to Motorola, this compiler also will simplify migration from other architectures like the 16-bit **fixed - point** 56300 family. Fundamentally, the SC140 is a **variable - length** execution set (VLES) with explicitly parallel-instruction computing (EPIC). "It combines the best of both...

14/3,K/5 (Item 3 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2006 The Gale Group. All rts. reserv.

06331306 Supplier Number: 54606036 (USE FORMAT 7 FOR FULLTEXT)
StarCore Reveals Its First DSP : Six-Issue VLIW Core Can Execute 1.2 Billion MACs/s, 3,000 MIPS.(StarCore SC140 digital signal processor)(Product Announcement)
Halfhill, Tom R.
Microprocessor Report, v13, n6, pNA
May 10, 1999
Language: English Record Type: Fulltext
Article Type: Product Announcement
Document Type: Newsletter; Refereed; Trade
Word Count: 2782

(USE FORMAT 7 FOR FULLTEXT)
TEXT:
...instructions per second at the core's target frequency of 300 MHz. In all, the **fixed - point** SC140 can execute six instructions per cycle-equivalent to 10 basic operations-for a blazing...

...SC140 and four times as many as the 'C6x. Moreover, TigerSharc DSPs will perform both **fixed - point** and floating-point operations, while the SC140 is strictly a **fixed - point** device. In this category at least, TigerSharc appears to have a meaner bite. Clock Speeds...s. Don't Call Me VLIW
StarCore prefers to describe its flavor of VLIW as **variable - length** execution sets (VLES), just as Intel prefers explicitly parallel instruction computing (EPIC) for IA-64...

...execution that seems to be a hallmark of new architectures. By grouping up to six **variable - length** instructions into a long instruction word, the SC140 mixes all of those philosophies with VLIW...all members of the family. This leads to the SC140's second advantage: code density. **Variable - length** bundles don't need to be fattened with the empty calories of NOPs. This is...

...The SC140 is a statically scheduled processor with 16 function units, optional instruction prefixes, and **variable - length** instruction bundles. It has about 180 instructions, not counting the numerous addressing variations. (Table 2...

...VLIW. It has been shipping for more than a year, and currently there are three **fixed - point** and two floating-point devices in the line. TI developed its tools in-house, with...

14/3,K/6 (Item 1 from file: 20)
DIALOG(R)File 20:Dialog Global Reporter
(c) 2006 Dialog. All rts. reserv.

02809578
Frontier Design Releases EDA Tools for the Creation of Re-usable DSP IP Cores
BUSINESS WIRE
September 14, 1998
JOURNAL CODE: WBWE LANGUAGE: English RECORD TYPE: FULLTEXT
WORD COUNT: 1484

... or a boolean (Bool). Bit-accurate Modeling of Quantization and Overflow -- Simply specifying fixed-word **length variable** is insufficient, however. The effects that result from using a fixed word-length must also...

...stored that exceeds the maximum or minimum value that can be represented with a given **fixed - point** data type. E.g. 100 cannot be stored in a Fix<8,2>, as it...

14/3,K/7 (Item 1 from file: 47)
DIALOG(R)File 47:Gale Group Magazine DB(TM)
(c) 2006 The Gale group. All rts. reserv.

03962105 SUPPLIER NUMBER: 14454524 (USE FORMAT 7 OR 9 FOR FULL TEXT)
SQL: putting up a good front. (Software Review) (overview of six evaluations of Structured Query Language tools)(includes related articles on highlights, Editors' Choices, Suitability to Task ratings, performance tests) (Evaluation)
Canter, Sheryl
PC Magazine, v12, n19, p237(21)
Nov 9, 1993
DOCUMENT TYPE: Evaluation ISSN: 0888-8507 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 4157 LINE COUNT: 00332

... customers, products, and sales. A variety of data types were represented, including double-precision floating- **point** , date, integer, **fixed - length** character string, **variable - length** character string, and BLOB (binary large object).

Applications were developed on a Compaq Deskpro 486...

14/3,K/8 (Item 2 from file: 47)
DIALOG(R)File 47:Gale Group Magazine DB(TM)
(c) 2006 The Gale group. All rts. reserv.

03085215 SUPPLIER NUMBER: 06467215 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Molecular phylogeny of the animal kingdom.
Field, Katharine G.; Olsen, Gary J.; Lane, David J.; Giovannoni, Stephen J.
; Ghiselin, Michael T.; Raff, Elizabeth C.; Pace, Norman R.; Raff, Rudolf
A.
Science, v239, n4841, p748(6)
Feb 12, 1988
CODEN: SCIEAS ISSN: 0036-8075 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT
WORD COUNT: 6242 LINE COUNT: 00514

... 12). Sequences were compared by a distance matrix method (14-16).
The average number of **fixed point** mutations per position separating
each pair of sequences (evolutionary distance) was estimated from the
number...

...mutations (17). Evolutionary distance estimates were used to infer
phylogenetic trees; branching order and branch **lengths** were **adjusted** so
that the pairwise evolutionary distance estimates were optimally reproduced
by the corresponding paths through...

14/3,K/9 (Item 3 from file: 47)
DIALOG(R)File 47:Gale Group Magazine DB(TM)
(c) 2006 The Gale group. All rts. reserv.

03084638 SUPPLIER NUMBER: 06264373 (USE FORMAT 7 OR 9 FOR FULL TEXT)
1988 the year of the data base: data base managers are taking center stage.
Bryan, Marvin
Personal Computing, v12, n1, p100(6)
Jan, 1988
ISSN: 0192-5490 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 4723 LINE COUNT: 00374

... Edition.
Of course, the Extended Edition will be fully relational. It will
support integer, floating **point**, packed decimal, **fixed** - and **variable** -
length character strings, and date, time, and time-stamp data types.
You'll be able to...

14/3,K/10 (Item 1 from file: 88)
DIALOG(R)File 88:Gale Group Business A.R.T.S.
(c) 2006 The Gale Group. All rts. reserv.

05605383 SUPPLIER NUMBER: 66111798
AUTOSCALER For C: An Optimizing Floating-Point to Integer C Program
Converter For Fixed-Point Digital Signal Processors.
Kum, Ki-Il; Kang, Jiyang; Sung, Wonyong
IEEE Transactions on Circuits and Systems-II: Analog and Digital..., 47, 9
, 840
Sept, 2000
ISSN: 1057-7130 LANGUAGE: English RECORD TYPE: Abstract

...AUTHOR ABSTRACT: programs to optimized integer C versions is developed for convenient programming and efficient use of **fixed - point** digital signal processors (DSPs). It not only converts data types and supports automatic scaling, but...

...output of this translator are ANSI C compliant programs, it can be used for any **fixed - point** DSP that supports ANSI C compiler. The number of shift operations that are required for scaling in the converted integer programs is reduced by equalizing the integer word- **lengths** of relevant **variables** and constants. For an optimal reduction, a cost function that represents the overhead of scaling...

14/3,K/11 (Item 1 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2006 The Gale Group. All rts. reserv.

08902361 SUPPLIER NUMBER: 18598293
Design reuse in today's DSP design methodology. (intellectual property-based design; digital signal processing)
Svoboda, Steve
Electronic Design, v44, n14, p101(7)
July 8, 1996
ISSN: 0013-4872 LANGUAGE: English RECORD TYPE: Fulltext; Abstract
WORD COUNT: 4148 LINE COUNT: 00343

... Decoder." The block contains C code that executes the Viterbi algorithm, and has parameters to **adjust** for register **length**, code rate, and so on. But the eventual implementation depends upon the particular design steps...

14/3,K/12 (Item 1 from file: 484)
DIALOG(R)File 484:Periodical Abs Plustext
(c) 2006 ProQuest. All rts. reserv.

03922828 (USE FORMAT 7 OR 9 FOR FULLTEXT)
How to pump a swing
Wirkus, Stephen; Rand, Richard; Ruina, Andy
College Mathematics Journal (PCMJ), v29 n4, p266-275, p.10
Sep 1998
ISSN: 0746-8342 JOURNAL CODE: PCMJ
DOCUMENT TYPE: Feature
LANGUAGE: English RECORD TYPE: Fulltext; Abstract
WORD COUNT: 1562

TEXT:

... system.
Pumping from a Standing Position
The pumped swing is modeled as a pendulum with **variable length** L. The rider is modeled as a point mass m, and L is the distance from the rider's center of mass to the **fixed** swing support **point** 0. Conservation of angular momentum for a point mass undergoing plane motion is (Formula Omitted...

14/3,K/13 (Item 1 from file: 647)
DIALOG(R)File 647:CMP Computer Fulltext
(c) 2006 CMP Media, LLC. All rts. reserv.

01040281 CMP ACCESSION NUMBER: EET19950116S0020

Two 'Lode' up on TCSI's new DSP core (Late News)

ASHOK BINDRA

ELECTRONIC ENGINEERING TIMES, 1995, n 831, PG12

PUBLICATION DATE: 950116

JOURNAL CODE: EET LANGUAGE: English

RECORD TYPE: Fulltext

SECTION HEADING: news

WORD COUNT: 581

... Two unnamed U.S. semiconductor manufacturers and Sanyo Electric Co. Ltd. have acquired the latest **adjustable** -bit- **length** (16-to-24-bit **fixed - point**) 20-ns core, the CD2450.

Clarkspur will have a gate-array version of its core...

?

File 348:EUROPEAN PATENTS 1978-2005/Dec W04

(c) 2005 European Patent Office

File 349:PCT FULLTEXT 1979-2005/UB=20051229,UT=20051222

(c) 2005 WIPO/Univentio

Set	Items	Description
S1	41707	(VARIABLE??? OR ADJUST????)(2N)(LENGTH?? OR DIGIT??)
S2	19965	FIX??(2N)POINT??
S3	597800	IMAG?? OR GRAPHIC???? OR PICTURE??
S4	206670	(CONVERT????? OR CONVER??????? OR TRANSFORM????? OR CHANG?- ??? OR TRANSLAT????)(7N)(FORMAT?? OR DESIGN?? OR STRUCTU???? - OR ARRANGE????? OR PATTERN???)
S5	215	DIRECT3D OR DIRECT???()3D??
S6	1002	AU=(WANG L? OR WANG, L? OR DENG K? OR DENG, K? OR GUO B? OR GUO, B? OR BUCKMAN J? OR BUCKMAN, J?)
S7	106135	(RENDER????? OR PROCESS????)(5N)S3
S8	1	S1(S)S2(S)S4(S)S5(S)S7
S9	8	S1(S)S2(S)S4(S)S7
S10	7	S9 NOT S8
S11	28	S1(S)S2(S)S3
S12	21	S11 NOT S9
S13	19	S12 NOT AD=20030913:20050106/PR
S14	6	S13(S)(S4 OR S5 OR S7)
S15	13	S13 NOT S14
S16	0	S6 AND S1 AND S2
?		

8/3,K/1 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2005 WIPO/Univentio. All rts. reserv.

00401863 **Image available**

APPARATUS AND METHOD FOR MANAGING AND DISTRIBUTING DESIGN AND MANUFACTURING
INFORMATION THROUGHOUT A SHEET METAL PRODUCTION FACILITY
APPAREIL ET METHODE CORRESPONDANTE PERMETTANT DE GERER ET DE REPARTIR UNE
INFORMATION RELATIVE A LA CONCEPTION ET A LA FABRICATION DANS UNE
INSTALLATION DE PRODUCTION DE TOLES

Patent Applicant/Assignee:

AMADA METRECS CO LTD,
AMADASOFT AMERICA INC,

Inventor(s):

HAZAMA Kensuke,
KASK Kalev,
SAKAI Satoshi,
SCHWALB Moshe Edward,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9742607 A2 19971113
Application: WO 97US7473 19970506 (PCT/WO US9707473)
Priority Application: US 9616958 19960506; US 96700671 19960731

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE

Publication Language: English

Fulltext Word Count: 149194

Fulltext Availability:

Detailed Description

Detailed Description

... illustrates an example of the various display menus and data tables
that may -23
be **graphically** displayed to aid a bending operator in selecting
tooling;
Fig. 32 illustrates an exemplary tool...of a collision check function of
the present
invention that may be implemented through a **graphical** user interface;
Figs. 36A and 36B illustrate a manipulation system of the invention for
manipulating the...match the bounding box of a minimal loop. By providing
such warnings relating to the **processed** 2-D, three view **image** to a
user, the user may be alerted of inconsistencies in the drawing data, and
the...view the different 2-D and 3-D representations of the - 122 part.
Software based **graphics** packages, such as OpenGL and **RenderWare**, may
be used to provide **graphical** computations. Such graphics libraries or
packages may be Windows based applications and can be used...
...with the data format utilized by the graphics library or package (e.g.,
OpenGL or **RenderWare**) that is utilized. Thereafter, the **graphics** data
may be **processed** in accordance with various programmed routines in
order to render the viewing mode (wire, solid...by moving a joystick or a
mouse), additional function calls may be made to the **graphics** library
to update the **rendered image**.

To provide the wire frame views of the part, the line entity data of the
...

...be derived for each of the faces 1 0 and provided as input to the

graphics package to **render** the view. **Graphics** packages such as OpenGL and **RenderWare** will take as input polygonal data and fill in the areas defined by the polygons...

...the processes and operations performed therein. The - 124 code in combination with an appropriate **graphics** package (such as OpenGL and **RenderWare**) may not only be used to render the different views (e.g., 2-D and...facenew - 1)*matrixdim ;
int *poldrow = poldmatrix + (faceold - 1)*matrixdim ;
first from the mismatches of the **fixed** faces.

Note: the diagonal terms of the matrix are always
iNoRelation.

therefore, they are skip...the flat version of the part. However, the part can have either the flat or **3D** version, but not both.

In other words, either all 3D-version of 3D-bodies must...

?

10/3,K/1 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2005 WIPO/Univentio. All rts. reserv.

01313061 **Image available**

**METHOD FOR AT LEAST PARTIALLY COMPENSATING FOR ERRORS IN INK DOT PLACEMENT
DUE TO ERRONEOUS ROTATIONAL DISPLACEMENT**

**PROCEDE POUR LA COMPENSATION AU MOINS PARTIELLE D'ERREURS DANS LE PLACEMENT
POINTS D'ENCRE DUES A UN DEPLACEMENT ROTATIONNEL ERRONE**

Patent Applicant/Assignee:

SILVERBROOK RESEARCH PTY LTD, 393 Darling Street, Balmain, New South
Wales 2041, AU, AU (Residence), AU (Nationality), (For all designated
states except: US)

Patent Applicant/Inventor:

WALMSLEY Simon Robert Walmsley, Silverbrook Research Pty Ltd, 393 Darling
Street, Balmain, New South Wales 2041, AU, AU (Residence), AU
(Nationality), (Designated only for: US)

SILVERBROOK Kia, Silverbrook Research Pty Ltd, 393 Darling Street,
Balmain, New South Wales 2041, AU, AU (Residence), AU (Nationality),
(Designated only for: US)

JACKSON PULVER Mark, Silverbrook Research Pty Ltd, 393 Darling Street,
Balmain, New South Wales 2041, AU, AU (Residence), AU (Nationality),
(Designated only for: US)

SHEAHAN John Robert, Silverbrook Research Pty Ltd, 393 Darling Street,
Balmain, New South Wales 2041, AU, AU (Residence), AU (Nationality),
(Designated only for: US)

PLUNKETT Richard Thomas, Silverbrook Research Pty Ltd, 393 Darling
Street, Balmain, New South Wales 2041, AU, AU (Residence), AU
(Nationality), (Designated only for: US)

WEBB Michael John, Silverbrook Research Pty Ltd, 393 Darling Street,
Balmain, New South Wales 2041, AU, AU (Residence), AU (Nationality),
(Designated only for: US)

MORPHETT Benjamin David, Silverbrook Research Pty Ltd, 393 Darling
Street, Balmain, New South Wales 2041, AU, AU (Residence), AU
(Nationality), (Designated only for: US)

Patent and Priority Information (Country, Number, Date):

Patent: WO 2005120835 A1 20051222 (WO 05120835)

Application: WO 2004AU706 20040527 (PCT/WO AU04000706)

Priority Application: WO 2004AU706 20040527

Designated States:

(All protection types applied unless otherwise stated - for applications
2004+)

AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NA NI NO NZ OM PG PH PL PT RO
RU SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PL PT RO
SE SI SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 618378

Fulltext Availability:

Claims

Claim

... dead nozzle test.

1) Run printhead nozzle test sequence

2) Either host or SoPEC CPU **converts** dead nozzle information into dead nozzle table. 3) Store dead nozzle table on host. 4...create the DRAM based commands. In this case the CPU will only be required to **point** the PCU to the correct location in DRAM to execute commands from.
111 21Firm requirements...

...usage. Compute-intensive operations for the CPU include authentication of downloaded programs and messages, and **image processing** functions such as cropping, rotation, whitebalance, color-space conversion etc. for printing images directly from...and the current DMA descriptor's SendZero register is set to 'I', then a zero **length** data packet is sent by asserting app err instead of app

gck. This indicates to...

10/3,K/2 (Item 2 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01129704

DEAD NOZZLE COMPENSATION

COMPENSATION D'UNE BUSE HORS ETAT DE FONCTIONNEMENT

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200450369 A1 20040617 (WO 0450369)

Application: WO 2003AU1616 20031202 (PCT/WO AU03001616)

Priority Application: AU 2002953134 20021202; AU 2002953135 20021202

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NI NO NZ OM PG PH PL PT RO RU

SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) BW GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
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Fulltext Word Count: 387411

Fulltext Availability:
Claims

Claim

... page rendering engine ASIC that takes compressed page images as input,
and produces decompressed page **images** at up to 6 channels of bi-level
dot data as output. The bi-level...

...office Bi-lithic printing environment:
CMY, for regular color printing. K, for black text, line **graphics** and
gray-scale printing. 0 0 IR (infrared), for Netpage-enabled [5]
applications. F (fixative...

10/3,K/3 (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01056423 **Image available**
**DERIVATIVES HAVING DEMAND-BASED, ADJUSTABLE RETURNS, AND TRADING EXCHANGE
THEREFOR
PRODUITS DERIVES PRESENTANT DES RENDEMENTS AJUSTABLES BASES SUR LA DEMANDE
ET ECHANGES COMMERCIAUX ASSOCIES**

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200385491 A2-A3 20031016 (WO 0385491)

Application: WO 2003US7990 20030313 (PCT/WO US03007990)

Priority Application: US 2002115505 20020402

Designated States:

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AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG
SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW

(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
SI SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 136258

Fulltext Availability:
Claims

Claim

... credit-related events can therefore be computed corresponding to any percentile in the distribution so **arranged** . For example, a CCAR value corresponding to a 95% statistical confidence level can be computed...

...would pay out fixed ratios should the underlying expire between two sets of strike prices. **Graphically** , digital calls, puts, spreads, and strips can have simple representations:

Table 6 1 - Digital Call...expressing buy digital option orders in terms of premium to be invested and expressing "sell" **digital** option orders in terms of notional payout, or notional payout less the premium received). In...

10/3,K/4 (Item 4 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00909145 **Image available**

PLANAR LASER ILLUMINATION AND IMAGING (PLIIM) SYSTEMS WITH INTEGRATED
DESPECKLING MECHANISMS PROVIDED THEREIN
SYSTEMES PLIIM D'ILLUMINATION ET D'IMAGERIE AU LASER PLANAIRE A MECANISME
DE DECHATOIEMENT INTEGRE

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Patent and Priority Information (Country, Number, Date):
 Patent: WO 200243195 A2-A3 20020530 (WO 0243195)
 Application: WO 2001US44011 20011121 (PCT/WO US0144011)
 Priority Application: US 2000721885 20001124; US 2001780027 20010209; US
 2001781665 20010212; US 2001883130 20010615; US 2001954477 20010917; US
 2001999687 20011031

Parent Application/Grant:
 Related by Continuation to: US 2001954477 20010917 (CIP)

Designated States:
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 AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
 EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
 LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL
 TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW
 (EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
 (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
 (AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
 (EA) AM AZ BY KG KZ MD RU TJ TM

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Fulltext Availability:
 Claims

Claim

... has an additional level of internal complexity. A zoom-type imaging subsystem is capable of **changing** its focal length over a given range; a longer focal length produces a smaller field...decreases as a function of increasing object distance. Therefore, in summary, where a fixed or **variable** focal **length** imaging subsystem is employed in the PLIIM system hereof, the planar laser beam focusing technique...sources) in

space over the photo-integration period of each detector element in the linear **image** detection array of the PLUM system, during which reflected laser illumination is received at the...

...a result of the present invention, image-based bar code symbol decoders and/or OCR **processors** operating on such digital **images** can be **processed** with significant reductions in error. The first generalized method above can be explained in terms...micro-oscillating the PLIB 393 prior to illuminating the target object. The lens array ring **structure** 392 can be made from a strip of holographic recording material 392A which has cylindrical...pattern samples which need to be generated per each photo-integration time interval of the **image** detection array can be experimentally determined without undue experimentation. However, for a particular degree of...

10/3,K/5 (Item 5 from file: 349)
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00761432

METHODS, CONCEPTS AND TECHNOLOGY FOR DYNAMIC COMPARISON OF PRODUCT FEATURES AND CUSTOMER PROFILE

PROCEDES, CONCEPTS ET TECHNIQUE DE COMPARAISON DYNAMIQUE DE CARACTERISTIQUES D'UN PRODUIT ET DU PROFIL DES CONSOMMATEURS

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200073958 A2 20001207 (WO 0073958)
Application: WO 2000US14459 20000524 (PCT/WO US0014459)
Priority Application: US 99320818 19990527

Designated States:

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AE AG AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM DZ EE ES
FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU
LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR
TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 151011

Fulltext Availability:

Detailed Description

Detailed Description

... released a Web browser called "Mosaic" that implemented a graphical user interface (QUI). Mosaic's **graphical** user interface was simple to

learn yet powerful. The Mosaic browser allows a user to...look into' a media file and understand its contents). For this reason, some of the **processes** that support multimedia content management must be handled differently.

The three major **processes** that are required to support media content management are.

- Storage management
- Metadata management
- Version control...

integrated suites of software, provide the basic functionality required to create documents, spreadsheets, and simple **graphics** or diagrams. More recently, the ability to access the Internet and browse electronic documentation has been added to the suite of productivity tools.

92

Specifically, productivity tools include.

- Spreadsheet
- Word **Processor**
- Graphics** Editor
- 0 Personal Organizer (may be linked to Group Scheduling)
- Methodology Browser
- Internet Access

These...or progress. The tool should support the project decisions regarding consistency.

Process Modeling

I 0 **Process** modeling tools provide a **graphical** depiction of the business functions and processes being supported by a system. The tool(s) ...must be taken into account when planning the work.

When classes and components are being **fixed** or modified, impact analysis tools are needed to see where the modified entity is being... capability of the tool and obtain user acceptance, rather than gathering business requirements and documenting **design** based on the requirements.

If the objective of the prototype is to document designs based...be pixelbased (bitmaps) or vector-based, each with their own advantages.

Pixel-based tools (traditional **graphics** and **image processing** tools) offer more **image** flexibility especially in terms of color gradation and shading, but produce relatively large files. This...

10/3,K/6 (Item 6 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00473016 **Image available**
A CAMERA WITH INTERNAL PRINTING SYSTEM
APPAREIL PHOTOGRAPHIQUE A SYSTEME D'IMPRESSION INTERNE
Patent Applicant/Assignee:
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SILVERBROOK Kia,
WALMSLEY Simon,
LAPSTUN Paul,
Inventor(s):
SILVERBROOK Kia,

WALMSLEY Simon,
LAPSTUN Paul,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9904368 A1 19990128
Application: WO 98AU544 19980715 (PCT/WO AU9800544)
Priority Application: AU 978003 19970715; AU 978005 19970715; AU 978031 19970715; AU 977991 19970715; AU 977998 19970715; AU 977988 19970715; AU 977993 19970715; AU 978012 19970715; AU 978017 19970715; AU 978014 19970715; AU 978025 19970715; AU 978032 19970715; AU 977999 19970715; AU 978024 19970715; AU 978016 19970715; AU 978030 19970715; AU 977938 19970715; AU 977997 19970715; AU 977979 19970715; AU 978015 19970715; AU 977978 19970715; AU 977982 19970715; AU 977989 19970715; AU 978019 19970715; AU 977980 19970715; AU 977942 19970715; AU 978018 19970715; AU 978021 19970715; AU 978000 19970715; AU 977940 19970715; AU 977939 19970715; AU 978020 19970715; AU 977985 19970715; AU 977987 19970715; AU 978022 19970715; AU 978029 19970715; AU 978023 19970715; AU 978028 19970715; AU 978027 19970715; AU 978026 19970715; AU 977983 19970715; AU 977986 19970715; AU 977981 19970715; AU 977977 19970715; AU 977934 19970715; AU 977990 19970715; AU 978497 19970811; AU 978505 19970811; AU 978498 19970811; AU 978504 19970811; AU 978501 19970811; AU 978500 19970811; AU 978502 19970811; AU 978499 19970811; AU 979395 19970923; AU 979404 19970923; AU 979394 19970923; AU 979396 19970923; AU 979397 19970923; AU 979398 19970923; AU 979399 19970923; AU 979400 19970923; AU 979401 19970923; AU 979402 19970923; AU 979403 19970923; AU 979405 19970923; AU 97959 19971216; AU 981397 19980119; AU 982370 19980316; AU 982371 19980316; AU 984094 19980612

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AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH GM
HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX
NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU ZW GH
GM KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH CY DE DK ES
FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW ML MR NE SN
TD TG

Publication Language: English

Fulltext Word Count: 191348

Fulltext Availability:

Detailed Description

Detailed Description

... of the garment and additionally, there is the fabric colours and style. Often, a fashion **designer** will try many different alternatives and may even attempt to draw the final fashion product...scene and store it on a storage device for later printing out.

Unfortunately, such an **arrangement** can be unduly cumbersome especially where it is desired to utilize the binocular system in...media.

In accordance with a further aspect of the present invention, there is provided a **digital** camera system comprising a sensing means for sensing an image; modification means for modifying the...IM, meaning that the target locator will always be catching up to the alternative Artcard **image** sensor pixel reader.

Processing the targets

The timing for sorting and checking the target numbers is trivial. The finding...a relatively inefficient method if necessary, yet still catch up quickly during the extracting data **process**.

Phase 2 - Decode Bit **Image**

Phase 2 is the non-real-time phase of alternative Artcard data recovery algorithm. At...sub-system that allows general hardware speed up of the following time-critical functions.

- 1) **Image** access mechanisms for general software **processing**
- 2) **Image** convolver.
- 3) Data driven image warper
- 4) Image scaling
- 5) Image tessellation
- 6) Affine transform...entries in the kernel. Since this is compute bound, it is appropriate to divide the **image** into multiple sections and **process** them in parallel on different ALU units.

On a 70 kernel, the time taken for...by 2:1 in each dimension produces an image 1/4 the original size. This **process** continues until the entire **image** is represented by a single pixel.

An image pyramid is constructed from an original image...

...This time is for a single color channel, 3 color channels require 0.034 seconds **processing** time.

General Data Driven **Image** Warping

The ACP 31 is able to carry out image warping manipulations of the input **image**. The principles of **image** warping are well-known in theory. One thorough text book reference on the **process** of warping is "Digital **Image** Warping" by George Wolberg published in 1990 by the IEEE Computer Society Press, Los Alamitos...2) $A = A - 1$

BNZ 0

Rest of processing Rest of processing

The second stage **processes** the actual pixels from the **image**, and uses 4 Adder ALUs.

Adder 1 Adder 2 Adder 3 Adder 4 Address Unit...the New Color Table 380, and written to a Sequential Write Iterator 383. The input **image** can be **processed** simultaneously in two halves to make effective use of memory bandwidth. The following lookup table...

10/3,K/7 (Item 7 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00401864 **Image available**

APPARATUS AND METHOD FOR GENERATING A SHEET-METAL BEND MODEL

APPAREIL ET PROCEDURE DE PRODUCTION D'UN MODELE DE CINTRAGE DE TOLE

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Inventor(s):

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Patent and Priority Information (Country, Number, Date):

Patent: WO 9742608 A1 19971113

Application: WO 97US7474 19970506 (PCT/WO US9707474)

Priority Application: US 9616958 19960506; US 96688860 19960731

Designated States:

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AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE

Publication Language: English
Fulltext Word Count: 146574

Fulltext Availability:
Detailed Description

Detailed Description

... through, for example,
object oriented programming techniques;
Fig. 18 illustrates a block diagram of the **structure** of the bend model viewer, in
accordance with another aspect of the present invention;
Fig...stages of a sheet metal part;
Fig. 47 is an exemplary menu screen diagram and **structure** of the present invention that may be provided and displayed to users for 2-D... commercially available SVGA monitor with, for example, 800 x 600 resolution. To support the various **graphics** and information that may be displayed on display 44, computer 48 may also include any commercially available graphics card such as a PCI **graphics** card. Further, computer 48 may include a Sound Blaster or compatible sound and game port adapter ...and retrieved by entering, for example, a predetermined reference number or code. The reference number or code may be entered manually (e.g., by keyboard or **digital** input pad) or by scanning a bar code with a bar code reader or scanner...a three bend box) that is related and within the same feature type. The hierarchical **structure** by which the 10 features/shapes are modified may be predetermined and developed based...

?

14/3,K/1 (Item 1 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00741338

Connectionless communications system, test method, and intra-station control system

Verbindungsloses Kommunikationssystem, Testmethode und Intra-Station-Steuerungssystem

Systeme de communication sans connection, methode de test et systeme de gestion intra-station

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LEGAL REPRESENTATIVE:

Ritter und Edler von Fischern, Bernhard, Dipl.-Ing. et al (9672), Hoffmann, Eitle & Partner, Patentanwalte, Arabellastrasse 4, D-81925 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 700229 A2 960306 (Basic)

EP 700229 A3 990203
APPLICATION (CC, No, Date): EP 95113111 950821;
PRIORITY (CC, No, Date): JP 94255120 940822
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS: H04Q-011/04
ABSTRACT WORD COUNT: 170

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	8491
SPEC A	(English)	EPAB96	164543
Total word count - document A			173034
Total word count - document B			0
Total word count - documents A + B			173034

...SPECIFICATION related to the control of a trailer in the PLCP, which is a physical layer **conversion** protocol interfaced in the DS3 **format**, that is, the digital signal level 3 format, is described below as one of the...refers to 14 nibbles for the third multiframe, the number of nibbles for the trailer **changes** 13 --> 14 --> 13 for the **pattern** P, and 13 --> 14 --> 14 for the pattern Q.
In the 125 (mu)sec period...

14/3,K/2 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00910207 **Image available**

CONTINUOUS PRODUCTION AND PACKAGING OF PERISHABLE GOODS IN LOW OXYGEN ENVIRONMENTS

PROCEDE DE PRODUCTION ET D'EMBALLAGE DE PRODUITS PERISSABLES DANS UNE ATMOSPHERE PAUVRE EN OXYGENE

Patent Applicant/Assignee:

SAFEFRESH TECHNOLOGIES LLC, 9772 S.E. 41st Street, Mercer Island, WA 98040, US, US (Residence), US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

GARWOOD Anthony J, 9772 S.E. 41st Street, Mercer Island, WA 98040, US, US (Residence), US (Nationality), (Designated only for: US)

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Legal Representative:

CRUZ Laura A (agent), Christensen O'Connor Johnson & Kindness PLLC, 1420 Fifth Avenue, Suite 2800, Seattle, WA 98101, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200244026 A1 20020606 (WO 0244026)

Application: WO 2001US45146 20011128 (PCT/WO US0145146)

Priority Application: US 2000724287 20001128; US 2000255684 20001213; US 2001286688 20010426; US 2001291872 20010517; US 2001299240 20010618; US 2001312176 20010813; US 2001314109 20010821; US 2001323629 20010919; US 2001335760 20011019

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR

LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SI
SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZM ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 197091

Fulltext Availability:

Claims

Claim

- ... primals made into food items used for human consumption are of irregular and inconvenient profile. **Conversely**, packaging trays that have been cost effectively and efficiently manufactured, are invariably rectangular and/or...of bacteria entering a "lag phase" when the environment in which it is placed, significantly **changes**. Eventually, the bacteria will adapt to the atmosphere that is present and commence normal reproduction...ground meat to ambient atmosphere including oxygen during the grinding and blending process at the **point** of slaughter. Furthermore, this process requires that relatively large quantities of ground beef are blended...meat can be vacuum packaged and stored in refrigerated facilities prior to delivery to the **point** of retail sale. However if the fat and lean content is not as required, then...almost certain predictability, emerge as the worst looking beef after removal from the master container. **Conversely**, the worst looking beef (i.e., beef colored by purple deoxymyoglobin) prior to packaging in...specific quantity of suitable gas dissolved therein plus additional quantity of suitable gas can be **arranged** so as to completely fill the suitable packaging of suitable size to provide a finished...wall 817, followed by an outwardly protruding first peak or ridge 825 that at one **point** can make contact with the outer cover 816. Continuing downward from the first peak, the...the flaps 1412, 1410, 1416 and 1414. Because flap 1416 may be a substantial mirror **image** of flap 1412 and flap 1414 may be a substantial mirror **image** of flap 1410, only flaps 1412 and 1410 will be discussed. But it is appreciated...
- ...are present that operate in substantially the same manner along the flaps that are mirror **images** of those discussed. A suitable heat source can be provided to activate the heat activated ...tray and the tray walls and base by any suitable "ink jet" apparatus. Furthermore, colored **graphic** printing and any desired information can be printed and/or applied to any desired surfaces...only two flaps are shown, it is apparent that a flap that is a mirror **image** of 3910 is located along the tray wall opposite the tray wall to which flap 3910 is attached. Likewise, a flap that is a mirror **image** of flap 3911 maybe attached to the opposite wall of the tray from flap 391 1. While the term mirror **image** has been used to describe the flaps not shown in FIGURE 127 it is apparent...
- ...to one or more flaps may not appear on the flap that is its mirror **image**. The tray with flaps can be either thermoformed or injection molded or produced in any...

00401842 **Image available**

APPARATUS AND METHOD FOR MANAGING AND DISTRIBUTING DESIGN AND MANUFACTURING
INFORMATION THROUGHOUT A SHEET METAL PRODUCTION FACILITY
APPAREIL ET METHODE CORRESPONDANTE PERMETTANT DE GERER ET DE REPARTIR UNE
INFORMATION RELATIVE A LA CONCEPTION ET A LA FABRICATION DANS UNE
INSTALLATION DE PRODUCTION DE TOLES

Patent Applicant/Assignee:

AMADA METRECS CO LTD,
AMADASOFT AMERICA INC,

Inventor(s):

HAZAMA Kensuke,
KASK Kalev,
SAKAI Satoshi,
SUBBARAMAN Anand Hariharan,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9742586 A1 19971113
Application: WO 97US7471 19970506 (PCT/WO US9707471)
Priority Application: US 9616958 19960506; US 96690671 19960731

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE

Publication Language: English

Fulltext Word Count: 146782

Fulltext Availability:

Detailed Description

Detailed Description

... adjusted scale-factor when mapping the joystick movements. By way of
non-limiting example, an **adjustment** factor of 3 may be used.

The present invention also includes a method for manipulating...

...of the invention, the predetermined viewing function may comprise a
rotating function, whereby the displayed **image** of ...to pass through a
center of the screen when the current view of the displayed **image** of
the part is determined to be a partial view.

In accordance with another aspect...with an aspect of the present
invention;

I 0 Fig. 17 illustrates an exemplary data **structure** and access
algorithm of the bend model that may be utilized when implementing the
present...may be displayed on display 44, personal computer 40 may also
include any commercially available **graphics** card such as a PCI
graphics card. Further, computer 40 may include a Sound Blaster or
compatible sound and game port...32, or
theymaybedownloadedfromatapeordisk.

Servermodule32may,forexample,interface with a CAD/CAM system located at,
for example, **design** office 10, or server module 32 may include a stand
alone CAD/CAM system. Further...be constructed. By analyzing the
connectivity and grouping of the entities in the connectivity graph
structure, server module 32 may group and define the views based on the
relative position and...match the bounding box of a minimal loop. By
providing such warnings relating to the **processed** 2-D, three view
image to a user, the user may be alerted of inconsistencies in the
drawing data, and...provided in the present invention is provided below
with reference to Figs.

Fundamental computer **graphics** and geometric modeling techniques, such as geometric transformations and 3-D geometry techniques, may be utilized ...

...and view the different 2-D and 3-D representations of the part. Software based **graphics** packages, such as OpenGL and **RenderWare**, may be used to provide **graphical** computations. Such graphics libraries or packages may be Windows based applications and can be used...

...with the data format utilized by the graphics library or package (e.g., OpenGL or **RenderWare**) that is utilized. Thereafter, the **graphics** data may be **processed** in accordance with various programmed routines in order to render the viewing mode (wire...representation by the user (e.g., by moving a joystick or a mouse), additional function calls may be made to the **graphics** library to update the **rendered image**.

To provide the wire frame views of the part, the line entity data of the ...

...polygons should be derived for each of the faces and provided as input to the **graphics** package to **render** the view. **Graphics** packages such as OpenGL and **RenderWare** will take as input polygonal data and fill in the areas defined by the polygons...

...relating to the processes and operations performed therein. The code in combination with an appropriate **graphics** package (such as OpenGL and **RenderWare**) may not only be used to render the different views (e.g., 2-D and...the respective Z-buffer depths of the points. The Z-buffer depth is used by **graphics** packages, such as OpenGL and **RenderWare**, to define the distance to each point from the viewpoint or camera position. The Z...movements received from the user controlled input device (e.g., the mouse or joystick device). A **graphics** package, such as OpenGL or **RenderWare**, may be provided to facilitate the update of the current view provided to the user...function calculates the similar index for the given set of faces up to the specified **fixed** faces.

Note: only half of the matrix are included in the calculation.

int direct calculate...

```
...facenew - 1)*matrixdim ;
int *poldrow = poldmatrix + (faceold - 1)*matrixdim ;
first from the mismatches of the fixed faces.
```

Note: the diagonal terms of the matrix are always iNoRelation.

therefore, they are skip...

```
...facenew - 1)*matrixdim
int *poldrow = poldmatrix + (faceold - 1)*matrixdim
first from the mismatches of the fixed faces.
```

Note: the diagonal terms of the matrix are always iNoRelation.

therefore, they are skip...

```
...acenewl
```

pcold[faceold]
use count specified fents matrix to get the FENTCOUNT
of the un- **fixed** face-S of b-oth matrices
int listdim = matrixdim -,4,qacefixed
pcnew[03 = count
specified...

14/3,K/4 (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00300850 **Image available**
UPDATE MECHANISM FOR COMPUTER STORAGE CONTAINER MANAGER
MOYEN DE MISE A JOUR POUR MODULE DE GESTION D'ELEMENTS DE STOCKAGE
D'ORDINATEURS

Patent Applicant/Assignee:

APPLE COMPUTER INC,

Inventor(s):

HARRIS Jared M,

RUBEN Ira L,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9519001 A1 19950713

Application: WO 95US196 19950104 (PCT/WO US9500196)

Priority Application: US 94177853 19940105

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AM AT AU BB BG BR BY CA CH CN CZ DE DK EE ES FI GB GE HU JP KE KG KP KR
KZ LK LR LT LU LV MD MG MN MW MX NL NO NZ PL PT RO RU SD SE SI SK TJ TT
UA UZ VN KE MW SD SZ AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE BF
BJ CF CG CI CM GA GN ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 119635

Fulltext Availability:

Claims

Claim

... only understand

the bytes if we know how they will be used. A

paragraph, an **image** , etc. can be-an object if it

contains enough information so that we know how...property, and that

property must have at least one value. Each value

consists of a **variable length** sequence of bytes.

The Container Manager knows very little about a

container beyond the objects...to fully understand how The Container

Manager works, but they do not significantly change

the **picture** given above.

Type and property descriptions. Each property

associated with a value is actually a...the Container Manager format, but
they

do not appear directly in the API. The only **points** at

which an application actually deals with anything

corresponding to an ID is when it...more generally, arbitrary

transformations) can be added without modifying the

library,

Usage E&am - Compressed, **Format Converted**

@ple 3

Arra Suppose the value which an application is

dealing with is actually an array of pixels. In addition to decompressing it, on a given platform we want to **convert** each pixel to a different **format**. The mechanisms described herein allow us to take two (or more) data transformations, such as compression and **format conversion**, and compose them together. Just as the application does not need to be aware of...

...The next step is to put the compressed pixel array out in a file, and **convert** it to a different **format** when it is read in. This is all supported using exactly the same composition as...including making it a base type of the "all of the above" type which adds **format conversion**. To illustrate the concept of base types, Fig. 1 is a symbolic diagram of a...

...Fig. 2 illustrates a more complex type tree. As shown in Fig. 2, the type "**format converted** compressed file type" 202 has two base types, "compressed file type" 204 and "**format conversion** type" 206. As with compressed file type 102 in Fig. 1, compressed file type 204...the following linear chain: file access type 206, compression type 208, compressed file type 204, **format conversion** type 206, and **format converted** compressed file type 202.

Format converted compressed file type 202 is the "top" type on the chain, and "file access" type 206 is the "bottom" type on the chain. Note that compressed file type 204 and **format converted** compressed file type 202 do not have handlers associated with them (let us assume), they...type WO 95/19001 PCT[US95/00196
57

approach. In many cases, developers want to **convert** their existing file **formats** into Container Manager containers. Ideally, they would like to keep the resulting files readable by...in bytes.

Because of the desire that the label format be stable, the container label **format** should not be **changed** in future versions to add additional information. instead, it can be added as values of...first, the logical structure of the stream, and second the actual physical representation.

Logical stream **Structure**. The following grammar describes the set of stream elements and how they can be combined...entries for standard objects are given, although they are not required, to provide a complete **picture** of what is going on. The examples are given in a tabular form of the...could consist of a stream of rich text, and the second object could be an **image** that is logically embedded in the text. Let us further suppose that the **image** has two alternate representations in different formats, each of which is a stream. This example...

...but that an entry is provided anyway. We also assume that the types of the **image** streams are not standard

objects, so that they must be provided,
Entry (a) is the...control block
416.

The table of contents (TOC) 414 is the set of
related data **structures** that organize objects by
object IDs, The requirement that objects be kept in
sorted order...container is assumed to be a sequence of bytes that the
API user wants to **convert** to container **format** . S/he uses
CMDefineValueData to create values for objects in the bytes. Other than
that...

14/3,K/5 (Item 4 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00234265 **Image available**

SYSTEM FOR DIVIDING PROCESSING TASKS INTO SIGNAL PROCESSOR AND
DECISION-MAKING MICROPROCESSOR INTERFACING
SYSTEME DE SEPARATION DES TACHES DE TRAITEMENT EN TACHES POUR INTERFACAGE
AVEC UN PROCESSEUR DE SIGNAUX ET UN MICROPROCESSEUR DE PRISE DE
DECISION

Patent Applicant/Assignee:

STAR SEMICONDUCTOR CORPORATION,

Inventor(s):

ROBINSON Jeffrey I,

ROUSE Keith,

KRASSOWSKI Andrew J,

MONTLICK Terry F,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9308524 A1 19930429

Application: WO 92US8954 19921014 (PCT/WO US9208954)

Priority Application: US 91776161 19911015

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AU CA JP KR AT BE CH DE DK ES FR GB GR IE IT LU MC NL SE

Publication Language: English

Fulltext Word Count: 219172

Fulltext Availability:

Claims

Claim

... Module

The MakeSDL module takes the basic files that capture and define the
signal processing **design** and **converts** them into a **format** that the
Schedule module can use. The MakeSDL module
takes the following input files:

The...code, calculates the chip resources required and inserts the
necessary phantom cells to glue the **design** together. Then the module
converts the code package, into a binary program file containing
executable instructions, and an associated data...chip and the data
associated with that program. The load file represents the signal
processing **design** specified by the **designer** using the graphical
design interface and filter and transfer function definitions, all
packaged in a format that can be...

...file contains a specification of data type for each symbol. Data types
may be integer, **fixed point** , hexadecimal, or undefined. SDI commands

are sensitive to the data types of symbols when accessing...block for a
sink cell illustrates the use of the micro keyword:
astriblockrasink [%subr--default, % length =128] (in,)
verify (1/olength>0 && %length<---512), 'Specffy length in range 1 to 512
...

```
...ptr=outvector  
micro variable outvectorMength];  
begin  
//code here  
end
```

The definition of outvector is micro **variable** outvector[% length]. The
micro keyword identifies the variable, outvectorMength], as available for
access from a microprocessor. The...a flow diagram of the SPROC and
microprocessor development environment is seen. At 2010, using **graphic**
entry packages such as "Drafe", "Annotate". "ERC" and "Netlist" which are
available from OrCad in...microprocessor which will act as a host for the
SPROC, the symbol file will be **translated** into appropriate file
formats . Thus, as shown in Fig.

10 symbol **translation** is accomplished at 2050. Source code in accord
with the preferred embodiment of the invention...

14/3,K/6 (Item 5 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00153060

PARALLEL MACHINE ARCHITECTURE FOR PRODUCTION RULE SYSTEMS
ARCHITECTURE DE MACHINE PARALLELE POUR DES SYSTEMES DE REGLES DE PRODUCTION

Patent Applicant/Assignee:

MARTIN MARIETTA ENERGY SYSTEMS INC,

Inventor(s):

ALLEN John Daniel Jr,

BUTLER Philip Lee,

Patent and Priority Information (Country, Number, Date):

Patent: WO 8809972 A1 19881215

Application: WO 88US1901 19880609 (PCT/WO US8801901)

Priority Application: US 87976 19870609

Designated States:

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prior to 2004)

AT BE CH DE FR GB IT JP LU NL SE

Publication Language: English

Fulltext Word Count: 138162

Fulltext Availability:

Detailed Description

Detailed Description

... being accessed through the network,, this
signal only comes true when all of the rule **processors** are
ready for a data transfer. Note that in the normal case
of a network...four bytes in width. The message
for "REMOVE" in Fig, 53 is always a constant **length** . In
i 5
the implemented embodiment, four bytes are utilized for
@sach field, a 32...language version of
the programs set forth in the appendix, The 11IMG11

signifies a binary **image** file. The RP's are then started and return a flag to tell the host...TASke is modified from screen 26 of the user variables initialized here.

i iince the **image** can be saved and to prevent (CR) etc, The argqments on the stack when this...SAVE-STATE) SAVE-RPFORTH tells the host to save tt

CR CR so Saving RKORTH **Image** Total bytes 0 HERE U. forth in the file

1RPFORTH.lMG1. I

HERE <SERVER-1...ry

owl

Pi (Compile to RM m CPIM EXTENSIONS) PLB#01/03/86)

M Rule **Processor Image** Save

M

M HEX

M 90TE PFS 24 ALLOT

PFSAVE PFS 24 ERASE

0052504B PF5...

?

15/3,K/1 (Item 1 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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01679260

Method and device for correcting lens aberrations in a stereo camera system with zoom

Verfahren und Vorrichtung zur Korrektur von Linsenfehler eines stereoskopischen Kamerasystems mit Zoom

Procede et dispositif de correction des aberrations de lentille d'un systeme de camera stereoscopique avec zoom

PATENT ASSIGNEE:

TOPCON CORPORATION, (1709271), No. 75-1, Hasunuma-cho, Itabashi-ku, Tokyo 174-8580, (JP), (Applicant designated States: all)

INVENTOR:

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Kochi, Nobuo, Topcon Corporation, 75-1, Hasunuma-cho, Itabashi-ku, Tokyo 174-8580, (JP)

Noma, Takayuki, Topcon Corporation, 75-1, Hasunuma-cho, Itabashi-ku, Tokyo 174-8580, (JP)

LEGAL REPRESENTATIVE:

HOFFMANN - EITLE (101511), Patent- und Rechtsanwälte Arabellastrasse 4, 81925 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 1378790 A2 040107 (Basic)

EP 1378790 A3 040225

APPLICATION (CC, No, Date): EP 2003014484 030702;

PRIORITY (CC, No, Date): JP 2002195058 020703

DESIGNATED STATES: AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES; FI; FR; GB; GR; HU; IE; IT; LI; LU; MC; NL; PT; RO; SE; SI; SK; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK

INTERNATIONAL PATENT CLASS: G03B-035/08; G01C-011/20

ABSTRACT WORD COUNT: 194

NOTE:

Figure number on first page: 5

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200402	1074
SPEC A	(English)	200402	13683
Total word count - document A			14757
Total word count - document B			0
Total word count - documents A + B			14757

...SPECIFICATION performed on a fixed focus stereo camera and a photograph of an object for stereo **image** measurement is taken at its **fixed** focal **point**. However, a **fixed** focus stereo camera cannot **adjust** the focal **length** depending upon the objects, so that the **image** may be out of focus or the photographed object may not be large enough to fill the **image** area under some photographing conditions. Thus, it is difficult to obtain a stereo **image** of quality sufficient to be used in stereo **image** measurement. In addition, when the camera or the lens is changed depending upon the size...

...cannot be easily performed with a fixed focus stereo camera, which prevents spreading of stereo **image** measurement.

In a multi-focus digital camera employing a so-called zoom lens, the lens...

15/3,K/2 (Item 2 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01478920

DISINFECTION THROUGH PACKAGING

DESINFEKTION DURCH VERPACKUNG

DESINFECTION AU TRAVERS D'UN EMBALLAGE

PATENT ASSIGNEE:

Atlantium Lasers Limited, (4454900), 4 Makarios III Av. 9th Floor Capital
Center, Nicosia, (CY), (Proprietor designated states: all)

INVENTOR:

TRIBELSKY, Zamir, 37 Hadvor st., 90805 Mevaseret Tzion, (IL)

ENDE, Michael, , 90855 Moshav Shoeva, (IL)

LEGAL REPRESENTATIVE:

Sollner, Udo, Dipl.-Ing. et al (83154), Reinhardt Sollner Ganahl, Hausen
5b, 85551 Kirchheim, (DE)

PATENT (CC, No, Kind, Date): EP 1334031 A2 030813 (Basic)

EP 1334031 B1 041229

WO 2002038447 020516

APPLICATION (CC, No, Date): EP 2001980884 011026; WO 2001IL995 011026

PRIORITY (CC, No, Date): IL 13928700 001026

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
LU; MC; NL; PT; SE; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: B65B-055/08; B65B-055/16

NOTE:

No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
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CLAIMS B	(English)	200453	1737
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CLAIMS B	(German)	200453	1731
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CLAIMS B	(French)	200453	1822
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SPEC B	(English)	200453	37386
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Total word count - document A	0
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Total word count - document B	42676
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Total word count - documents A + B	42676
------------------------------------	-------

...SPECIFICATION point where rays of light meet which have been converged
by a lens. verb: to **adjust** focal **length** for the clearest **image** .

focal point in the context of the present invention means (same as first
definition under...

...in the context of the present invention means the number of light waves
passing a **fixed point** unit of time, or the number of complete
vibrations in that period of time.

gain...

...invention means the approved international term, abbreviated hz, which
replaces cps for cycles per second.

image in the context of the present invention means the optical
reproduction of an object, produced by a lens or mirror. a typical
positive lens converges rays to form a "real" **image** which can be
photographed. a negative lens spreads rays to form a "virtual" **image**
which can't be projected.

incident light in the context of the present invention means...

15/3,K/3 (Item 3 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00846435

Control system for increasing safety in a fast vehicle, especially for a vehicle guided by an operator who may be located inside or outside the vehicle, installation for guiding a vehicle and method for controlling a vehicle by a corresponding system

Steuersystem zum Erhoehen der Sicherheit eines schnellen Fahrzeugs, insbesondere eines Fahrzeugs, das von einem Bediener gesteuert wird, der sich innerhalb oder ausserhalb des Fahrzeugs befindet, Einrichtung zum Fuhren und Verfahren zum Steuern eines Fahrzeugs auf der Basis eines derartigen Systems

Systeme de commande pour la securisation d'un vehicule rapide, notamment guide par un operateur embarque ou non dans ledit vehicule, installation pour le guidage d'un vehicule et procede de commande d'un vehicule par un systeme correspondant

PATENT ASSIGNEE:

THALES SYSTEMES AEROPORTES S.A., (2790343), 55 quai Marcel Dassault, 92210 Saint-Cloud, (FR), (Proprietor designated states: all)

INVENTOR:

Le Gusquet, Frederic, 81, rue Belliard, 75018 Paris, (FR)
Tissedre, Marc, 35, rue de l'Esperance, 75013 Paris, (FR)
Saglio, Christophe, 4, rue Paul Bourget, 92130 Antony, (FR)
Salanson, Philippe, 8, rue Montbauron, 78000 Versailles, (FR)

LEGAL REPRESENTATIVE:

Placais, Jean-Yves et al (17891), Cabinet Netter, 36, avenue Hoche, 75008 Paris, (FR)

PATENT (CC, No, Kind, Date): EP 781679 A1 970702 (Basic)
EP 781679 B1 030305

APPLICATION (CC, No, Date): EP 96402697 961211;

PRIORITY (CC, No, Date): FR 9515579 951227

DESIGNATED STATES: DE; ES; GB; IT

INTERNATIONAL PATENT CLASS: B60K-031/00; G05D-001/00

TRANSLATED ABSTRACT WORD COUNT: 109

ABSTRACT WORD COUNT: 111

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): French; French; French

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(French)	EPAB97	2953
CLAIMS B	(English)	200310	3144
CLAIMS B	(German)	200310	2767
CLAIMS B	(French)	200310	2993
SPEC A	(French)	EPAB97	9410
SPEC B	(French)	200310	9419
Total word count - document A			12366
Total word count - document B			18323
Total word count - documents A + B			30689

...CLAIMS mode (MOD2) the joystick (JOY) comprises means capable of designating an operator segment (SGOP), of **variable length** , on the displayed **image** , having first (EX1) and second (EX2) movable

ends, the initial movable end (EX1) being connected to a **fixed point** (PF) of the **image** corresponding to a chosen **fixed point** of the vehicle and the final end (EX2) defining the point in the environment to...

15/3,K/4 (Item 4 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00646482

Method for non exhaustive motion estimation which times out.

Verfahren zur nicht-erschöpfenden Bewegungsschätzung mit Sperrzeit.

Procede pour l'estimation non-exhaustive de mouvement avec delai d'attente.

PATENT ASSIGNEE:

Intel Corporation, (322937), 2200 Mission College Blvd., Santa Clara, CA
95052-8119, (US), (applicant designated states: DE;FR;GB;IT;NL;SE)

INVENTOR:

Michael, Keith, 17712 S.W. Beaver Court, Beaverton, Oregon 97006, (US)

LEGAL REPRESENTATIVE:

von Bezold, Dieter, Dr. (12341), Dr. Dieter von Bezold Dipl.-Ing. Peter
Schutz Dipl.-Ing. Wolfgang Heusler Brienner Strasse 52, D-80333 Munchen
, (DE)

PATENT (CC, No, Kind, Date): EP 624984 A2 941117 (Basic)
EP 624984 A3 960731

APPLICATION (CC, No, Date): EP 94107379 940511;

PRIORITY (CC, No, Date): US 61717 930513

DESIGNATED STATES: DE; FR; GB; IT; NL; SE

INTERNATIONAL PATENT CLASS: H04N-007/137; H04N-007/30; H04N-007/36;

H04N-007/50; G06F-015/332;

ABSTRACT WORD COUNT: 204

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF2	477
SPEC A	(English)	EPABF2	17661
Total word count - document A			18138
Total word count - document B			0
Total word count - documents A + B			18138

...SPECIFICATION encode input buffer data structure 400.

Encode input buffer data structure 400 also contains original **image** block address pointer 404. Original **image** block address pointer 404 points to the original location of a block of data in current **image** block 326. It will be understood that original **image** block pointer 404 also points to current/companied **image** block 302 when **image** blocks 302, 326 are in the same physical memory because the accompanied **image** received by way of line 328 overwrites the current **image**. Thus the **image** being encoded is the original **image** which may be the same as the current **image**. It will also be understood that pointer 404 **points** to **fixed** length data rather than **variable** length data because the **image** of block 302 is not yet encoded. Previous **image** block address pointer 406 points to the location of the previous **image** in block 314.

Referring now to Fig. 5, there is shown encode output buffer data...

15/3,K/5 (Item 5 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00624860

DEVICE FOR SELF-AJUSTING THE LENGTH OF CONTROL CABLES

SELBSTEINSTELLENDEN VORRICHTUNG FÜR DIE LÄNGE VON STEUERKABELN

DISPOSITIF D'AUTO-AJUSTEMENT DE LA LONGUEUR DE CABLES DE COMMANDE

PATENT ASSIGNEE:

FICO CABLES, S.A., (1667581), Avenida Castellbisbal, E-08301 Rubí, (ES),
(applicant designated states: DE;ES;FR;GB;IT;PT;SE)

INVENTOR:

GABAS CEBOLLERO, Carlos, Calle Rocafort, 180, E-08029 Barcelona, (ES)

LEGAL REPRESENTATIVE:

SUGRANES - VERDONCES - FERREGUELA (101211), Calle Provenza, 304, 08008
Barcelona, (ES)

PATENT (CC, No, Kind, Date): EP 619437 A1 941012 (Basic)

EP 619437 B1 970226

WO 9410467 940511

APPLICATION (CC, No, Date): EP 93921934 931011; WO 93ES84 931011

PRIORITY (CC, No, Date): ES 922141 921026

DESIGNATED STATES: DE; ES; FR; GB; IT; PT; SE

INTERNATIONAL PATENT CLASS: F16C-001/22;

ABSTRACT WORD COUNT: 166

LANGUAGE (Publication,Procedural,Application): English; English; Spanish

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
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CLAIMS A	(English)	EPABF2	418
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CLAIMS B	(English)	EPAB97	415
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CLAIMS B	(German)	EPAB97	393
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CLAIMS B	(French)	EPAB97	454
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SPEC A	(English)	EPABF2	3424
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SPEC B	(English)	EPAB97	3489
--------	-----------	--------	------

Total word count - document A	3843
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Total word count - document B	4751
-------------------------------	------

Total word count - documents A + B	8594
------------------------------------	------

...ABSTRACT regulation rod (BR). The main body (CB) has means (2) for its anchoring at its **fixed point** and means for the retention (MR) of the rod (VR) which comprise two extensions (5...

...a threading (12) which matches the threadings (6,6') of said extensions (5,5').When **adjusting** the **length** of the control cable, the rod (VR) is introduced inside the main body (CB) against...

...which set the position reached by the rod (VR). Application to the car industry. (see **image** in original document)

15/3,K/6 (Item 6 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

00503205

SELF-ADJUSTMENT DEVICE FOR ADJUSTING THE LENGTH OF CONTROL CABLES.

STEUERSEILSELBSTEINSTELLVORRICHTUNG.

DISPOSITIF D'AUTO-AJUSTEMENT PERMETTANT DE REGLER LA LONGUEUR DE CABLES DE COMMANDE.

PATENT ASSIGNEE:

FICO-CABLES, S.A., (1164051), Avenida Castellbisbal s/n, E-08191 Rubí, (ES), (applicant designated states: AT;BE;DE;FR;GB;IT;NL;SE)

INVENTOR:

GABAS CEBOLLERO, Carlos, Calle Rocafort, 180, E-08029 Barcelona, (ES)

ROCA CABESTANY, Agustin, Paseo del Ferrocarril, 1, E-08191 Rubi, (ES)
LEGAL REPRESENTATIVE:

Schwabe - Sandmair - Marx (100951), Stuntzstrasse 16, D-81677 Munchen,
(DE)

PATENT (CC, No, Kind, Date): EP 479980 A1 920415 (Basic)
EP 485540 A1 920520
EP 485540 B1 950802
WO 9117365 911114

APPLICATION (CC, No, Date): EP 91907482 910409; WO 91ES19 910409

PRIORITY (CC, No, Date): ES 901269 900507

DESIGNATED STATES: AT; BE; DE; FR; GB; IT; NL; SE

INTERNATIONAL PATENT CLASS: F16C-001/22;

ABSTRACT WORD COUNT: 163

NOTE:

No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; Spanish

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	807
CLAIMS B	(English)	EPAB95	835
CLAIMS B	(German)	EPAB95	853
CLAIMS B	(French)	EPAB95	898
SPEC A	(English)	EPABF1	2819
SPEC B	(English)	EPAB95	2802
Total word count - document A			3626
Total word count - document B			5388
Total word count - documents A + B			9014

...ABSTRACT A1

The self-adjustment device for **adjusting** the **length** of control
cables is comprised of a main body (CP) wherein are fitted the retention

...
...retention threading (7), traversing it, provided with means (1,2) for
its securing at a **fixed point**, and also comprised of a regulation
spring (RR) arranged coaxially with respect to said regulation...

...free or fix the position of said rod (VR) inside the main body (CP).
(see **image** in original document)

15/3,K/7 (Item 7 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00422988

Adjustable cable sheath terminal.

Einstellbares Kabelhullenendstuck.

Extremite de gaine de cable ajustable.

PATENT ASSIGNEE:

FICO-CABLES, S.A., (1164051), Avenida Castellbisbal s/n, E-08191 Rubi,
(ES), (applicant designated states: AT;BE;DE;FR;GB;IT;NL;SE)

INVENTOR:

Roca Cabestany, Agustin, Paseo Ferrocarril No. 1 2o 6a, E-08191 Rubi,
Barcelona, (ES)

LEGAL REPRESENTATIVE:

Schwabe - Sandmair - Marx (100951), Stuntzstrasse 16, D-81677 Munchen,
(DE)

PATENT (CC, No, Kind, Date): EP 431307 A1 910612 (Basic)
EP 431307 B1 940209

APPLICATION (CC, No, Date): EP 90120734 901029;

PRIORITY (CC, No, Date): ES 894147 891205
DESIGNATED STATES: AT; BE; DE; FR; GB; IT; NL; SE
INTERNATIONAL PATENT CLASS: F16C-001/22;
ABSTRACT WORD COUNT: 141
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1126
CLAIMS B	(German)	EPBBF1	1024
CLAIMS B	(French)	EPBBF1	1157
SPEC B	(English)	EPBBF1	3537
Total word count - document A			0
Total word count - document B			6844
Total word count - documents A + B			6844

...ABSTRACT be used particularly in the automobile industry. To solve the problems which arise from the **adjustment** of the **length** of cables having a protective sheath an adjustable cable-sheath terminal is provided which is...

...essentially cylindrical hollow main body (CP) provided with mechanical means (AN) for anchorage to a **fixed point**, by an adjusting rod (RE) having an external screw thread (1) and which, fixedly attached...

...fingers (GR) which may firmly retain said adjusting rod (RE) in the desired position. (see **image** in original document)

15/3,K/8 (Item 8 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00362874

Data storage, retrieval and transmission in computer systems.
Datenspeicherung, -wiederauffindung und -ubertragung in Rechnersystemen.
Archivage, ressaisie et transmission de donnees dans des systemes d'ordinateurs.

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
Armonk, N.Y. 10504, (US), (applicant designated states:
BE;CH;DE;ES;FR;GB;IT;LI;NL;SE)

INVENTOR:

Alexander, Virginia Lee, 10506 Mourning Dove Drive, Austin Texas 78750,
(US)
Gaudet, James Louis, 1500 Creek Hollow, Austin Texas 78754, (US)
Jordan II, Lloyd Eugene, 13505 Bayswater Garden, Austin Texas 78729, (US)
Hernandez, Raymond, 13304 Council Bluff Drive, Austin Texas 78727, (US)

LEGAL REPRESENTATIVE:

Blakemore, Frederick Norman et al (28381), IBM United Kingdom Limited
Intellectual Property Department Hursley Park, Winchester Hampshire
SO21 2JN, (GB)

PATENT (CC, No, Kind, Date): EP 336586 A2 891011 (Basic)
EP 336586 A3 921028

APPLICATION (CC, No, Date): EP 89302584 890316;

PRIORITY (CC, No, Date): US 179316 880408

DESIGNATED STATES: BE; CH; DE; ES; FR; GB; IT; LI; NL; SE

INTERNATIONAL PATENT CLASS: G06F-015/40;

ABSTRACT WORD COUNT: 219

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	859
SPEC A	(English)	EPABF1	4262
Total word count - document A			5121
Total word count - document B			0
Total word count - documents A + B			5121

...SPECIFICATION All data items for a column must be of the identical data type (integer, floating **point** number, **fixed length** character, **variable length graphic**, etc.).

Consequently, any specific data item in the table belongs to exactly one row and...

15/3,K/9 (Item 9 from file: 348)
 DIALOG(R)File 348:EUROPEAN PATENTS
 (c) 2005 European Patent Office. All rts. reserv.

00307004

A method of providing score lines in packaging material.

Verfahren zur Herstellung von Einkerbungen in Verpackungsmaterial.

Procede de realisation d'entailles dans des matieres d'emballage.

PATENT ASSIGNEE:

LEEWARDER PAPIERWARENFABRIEK B.V., (1011940), Harlingertrekweg 45,
 NL-8913 HR Leeuwarden, (NL), (applicant designated states:
 AT;BE;CH;DE;ES;FR;GB;GR;IT;LI;LU;NL;SE)

INVENTOR:

Huizinga, Gert Jan, Abeelstraat 25, NL-8924 BR Leeuwarden, (NL)

LEGAL REPRESENTATIVE:

Smulders, Theodorus A.H.J., Ir. et al (21191), Vereenigde Octrooibureaux
 Nieuwe Parklaan 97, NL-2587 BN 's-Gravenhage, (NL)

PATENT (CC, No, Kind, Date): EP 357841 A1 900314 (Basic)
 EP 357841 B1 940302

APPLICATION (CC, No, Date): EP 88201929 880907;

PRIORITY (CC, No, Date): EP 88201929 880907

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: B29C-053/06; B31B-001/25; B23K-026/00;

ABSTRACT WORD COUNT: 89

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	323
CLAIMS B	(German)	EPBBF1	261
CLAIMS B	(French)	EPBBF1	347
SPEC B	(English)	EPBBF1	1758
Total word count - document A			0
Total word count - document B			2689
Total word count - documents A + B			2689

...CLAIMS which is movable relative to said packaging material in two mutually perpendicular directions from a **fixed point** in response to control signals, which define the path of the score line to be...

...first side, thereby forming score lines thereon, said web having a recurring pattern of printed **images** appearing on one side thereof
 - moving said first laser beam in response to pattern dependent...

...provided in registration with said pattern and being responsive to the speed of the web
 - **adjusting** the focal **length** of said laser beam so that the

focus of the first laser beam is maintained...

...score lines are being formed thereon

- monitoring positional irregularities in the recurring pattern of printed **images** on said one side of the web and
- adjusting in response to said monitoring, the...

15/3,K/10 (Item 10 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00211730

White balance adjusting device of color video camera
Vorrichtung zur Weissabgleicheinstellung einer Farbvideokamera
Dispositif d'ajustage d'equilibre des blancs dans une camera de video en couleurs

PATENT ASSIGNEE:

HITACHI, LTD., (204144), 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo
100, (JP), (applicant designated states: DE;FR;GB)

INVENTOR:

Murakami, Toshio, San Totsuka 611 4208-10 Totsukacho, Totsuka-ku Yokohama
, (JP)

Takagi, Yasushi, Fujimiryō 415 1545 Yoshidacho, Totsuka-ku Yokohama, (JP)

Ohtsubo, Hiroyasu, Meishinryo, 426-3, Tabiko, Katsuta-shi, (JP)

Satoh, Yutaka, Meishinryo, 426-3, Tabiko, Katsuta-shi, (JP)

LEGAL REPRESENTATIVE:

Altenburg, Udo, Dipl.-Phys. et al (1266), Patent- und Rechtsanwälte
Bardehle . Pagenberg . Dost . Altenburg . Frohwitter . Geissler &
Partner Postfach 86 06 20, D-81633 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 224904 A2 870610 (Basic)

EP 224904 A3 890315

EP 224904 B1 960306

APPLICATION (CC, No, Date): EP 86116706 861202;

PRIORITY (CC, No, Date): JP 85271423 851204; JP 85285523 851220; JP
86131856 860609

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: H04N-009/73;

ABSTRACT WORD COUNT: 174

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	1397
CLAIMS B	(English)	EPAB96	481
CLAIMS B	(German)	EPAB96	384
CLAIMS B	(French)	EPAB96	610
SPEC A	(English)	EPABF1	3949
SPEC B	(English)	EPAB96	4347
Total word count - document A			5346
Total word count - document B			5822
Total word count - documents A + B			11168

...SPECIFICATION digitization, a memory function is provided. Thus, the control loop can be stopped at any **point** to **fix** the white balance. Thus, a white object is photographed to determine a theoretical white balance...

...22 is provided to control the selection of the operation mode and the white balance **adjustment** of the **digital** circuit. In the start/stop control circuit 22, the output from the up/down selector...be held so

that the white balance is fixed. A reset circuit 23 prevents abnormal **image** at the power-on or mode selection. It resets the up/down counter 20 by...

...SPECIFICATION digitization, a memory function is provided. Thus, the control loop can be stopped at any **point** to **fix** the white balance. Thus, a white object is photographed to determine a theoretical white balance...

...22 is provided to control the selection of the operation mode and the white balance **adjustment** of the **digital** circuit. In the start/stop control circuit 22, the output from the up/down selector...

...be held so that the white balance is fixed. A reset circuit 23 prevents abnormal **image** at the power-on or mode selection. It resets the up/down counter 20 by...

15/3,K/11 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00975264 **Image available**

INTER-CHIP COMMUNICATION SYSTEM

SYSTEME DE COMMUNICATION INTER-PUCES

Patent Applicant/Assignee:

AXIS SYSTEMS INC, 209 Java Drive, Sunnyvale, CA 94085, US, US (Residence)
, US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

TSENG Ping-Sheng, 992 Coeur d'Alene Way, Sunnyvale, CA 94087, US, US
(Residence), US (Nationality), (Designated only for: US)

Legal Representative:

CHOU Chien-Wei (Chris) (et al) (agent), Oppenheimer Wolff & Donnelly LLP,
1400 Page Mill Road, Palo Alto, CA 94304, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200305212 A1 20030116 (WO 0305212)

Application: WO 2001US26625 20010823 (PCT/WO US0126625)

Priority Application: US 2001900124 20010706

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS
LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ
TM TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 110858

Fulltext Availability:
Detailed Description

Detailed Description

... READ;

55

S space write pointer: H2S-move = (SPACE-index #H2S) & WRITE;

CLK space write **pointer** : CLK-move = (SPACE-index = #CLK)& WRITE;

This is the code equivalent for the logic diagram...

15/3,K/12 (Item 2 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00942062 **Image available**
DIGITAL OPTIONS HAVING DEMAND-BASED, ADJUSTABLE RETURNS, AND TRADING
EXCHANGE THEREFOR
OPTIONS NUMERIQUES COMPORTANT DES RETOURS AJUSTABLES A BASE DE DEMANDE ET
BOURSE D'ECHANGE A CET EFFET

Patent Applicant/Assignee:

LONGITUDE INC, 650 Fifth Avenue, New York, NY 10019, US, US (Residence),
US (Nationality)

Inventor(s):

LANGE Jeffrey, 3 East 84th Street, Apt. 3, New York, NY 10028, US,

Legal Representative:

WEISS Charles A (et al) (agent), Kenyon & Kenyon, One Broadway, New York,
NY 10004, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200274047 A2-A3 20020926 (WO 0274047)

Application: WO 2002US7480 20020311 (PCT/WO US0207480)

Priority Application: US 2001809025 20010316

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SI
SK SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 85860

Fulltext Availability:

Claims

Claim

... of the dependent variables in the selected equation; (c) calculating
the value of the independent **variable** in the selected equation
responsive to the currently assigned values of each the dependent
variables...would pay out fixed ratios should the underlying expire
between two sets of strike prices. **Graphically** , digital calls, puts,
spreads, and strips can have simple
representations:
Table 6 1
Digital Call...trading period for returns to adjust to a fair value, then
FIG. 8 provides a graphical depiction, in terms of the - 180 percentage
of the implied state probability, of the maximum...

15/3,K/13 (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00360923 **Image available**

METHOD OF IMAGING POINTS IN A VIDEO IMAGE, AND A DEVICE FOR CARRYING OUT
THE METHOD

PROCEDE DE REPRODUCTION DES POINTS D'UNE IMAGE VIDEO ET DISPOSITIF
CORRESPONDANT

Patent Applicant/Assignee:

CARL ZEISS JENA GMBH,
LDT GMBH & CO LASER-DISPLAY-TECHNOLOGIE KG,
RODER Rolf,
DETER Christhard,

Inventor(s):

RODER Rolf,
DETER Christhard,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9701248 A1 19970109
Application: WO 96EP1751 19960426 (PCT/WO EP9601751)
Priority Application: DE 19522698 19950622

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

BR CA CN FI JP KR RU SI US AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL
PT SE

Publication Language: German

Fulltext Word Count: 7049

English Abstract

The invention concerns a method of imaging points in a video **image**
using a light beam (40, 40', 64, 64') whose intensity is controlled as a
function...

...the lens system (50) or part (78) of the lens system (50) to have a
variable focal length and, when the focal length is varied, the focal
point on the object side to be maintained at a **fixed point** . The
invention also concerns a device for imaging points in a video **image** on
a screen (54) using a deflection device which deflects a light beam,
whose intensity is controlled as a function of the brightness of the
points, for line-scanning and **picture** scanning. The device has a lens
(63, 63') or lens system (50) or part (78)...

...or the part (78) of the lens system (50) on the object side at a **fixed
point** .

?

File 344:Chinese Patents Abs Aug 1985-2005/May
(c) 2005 European Patent Office
File 347:JAPIO Nov 1976-2005/Aug(Updated 051205)
(c) 2005 JPO & JAPIO
File 350:Derwent WPIX 1963-2005/UD,UM &UP=200601
(c) 2006 Thomson Derwent

Set	Items	Description
S1	44842	(VARIABL??? OR ADJUST????)(2N)(LENGTH?? OR DIGIT??)
S2	14755	FIX??(2N)POINT??
S3	1601732	IMAG?? OR GRAPHIC???? OR PICTURE??
S4	142424	(CONVERT????? OR CONVER??????? OR TRANSFORM????? OR CHANG?- ??? OR TRANSLAT????)(7N)(FORMAT?? OR DESIGN?? OR STRUCTU???? - OR ARRANGE????? OR PATTERN???)
S5	11	DIRECT3D OR DIRECT???()3D??
S6	4529	AU=(WANG L? OR WANG, L? OR DENG K? OR DENG, K? OR GUO B? OR GUO, B? OR BUCKMAN J? OR BUCKMAN, J?)
S7	254240	(RENDER????? OR PROCESS????)(5N)S3
S8	1	S1 AND S2 AND S4 AND S5 AND S7
S9	1	S1 AND S2 AND S4 AND S7
S10	0	S9 NOT S8
S11	13	S1 AND S2 AND S3
S12	12	S11 NOT S8
S13	1	S6 AND S1 AND S2
S14	0	S13 NOT S8
S15	1	S1 AND S2 AND S5

8/3,K/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

016936736 **Image available**
WPI Acc No: 2005-261046/200527
XRPX Acc No: N05-214323

Graphics rendering method for embedded device e.g. mobile computing device, involves processing rendering data in variable - length fixed - point format, and rendering processed rendering data on embedded device

Patent Assignee: MICROSOFT CORP (MICT)
Inventor: BUCKMAN J W; DENG K; GUO B; WANG L
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20050062762	A1	20050324	US 2003661055	A	20030913	200527 B

Priority Applications (No Type Date): US 2003661055 A 20030913

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20050062762	A1		28	G09G-005/00	

Graphics rendering method for embedded device e.g. mobile computing device, involves processing rendering data in variable - length fixed - point format, and rendering processed rendering data on embedded device

Abstract (Basic):

... The method involves inputting a rendering data in one format .
The rendering data from the format is converted into a variable length fixed - point format . The rendering data in the variable - length fixed - point format is processed. The processed rendering data is rendered on an embedded device. A mathematical library is created for processing the rendering data in the variable - length fixed - point format.

... a computer-readable medium having computer-executable instructions for performing the computer-implemented method for rendering graphics
(...

...B) a graphics rendering system for an embedded computing device...

...Used for rendering graphics on an embedded device e.g. mobile computing device...

...The data is rendered in the variable - length fixed - point format, thus enabling efficient and fast graphics rendering in the embedded devices. The graphics rendering method are software implemented, thus avoiding requirement of powerful graphics and processing hardware...

...The drawing shows an implementation of normalized vectors in a Direct3D mobile phone model

?

12/3,K/1 (Item 1 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2005 JPO & JAPIO. All rts. reserv.

05175237 **Image available**
PICTURE DATA ENCODING AND DECODING DEVICE

PUB. NO.: 08-130737 [JP 8130737 A]
PUBLISHED: May 21, 1996 (19960521)
INVENTOR(s): NAKAGAWA AKIKO
APPLICANT(s): MATSUSHITA ELECTRIC IND CO LTD [000582] (A Japanese Company
or Corporation), JP (Japan)
APPL. NO.: 06-292007 [JP 94292007]
FILED: November 02, 1994 (19941102)

PICTURE DATA ENCODING AND DECODING DEVICE

ABSTRACT

PURPOSE: To provide a **picture** data encoding and decoding device for eliminating the mismatch of the DCT of an encoder...

...CONSTITUTION: In this **picture** data encoding and decoding device provided with the DCT processing means 2 of **picture** data divided into blocks, the quantization means 3 of a DCT coefficient inside the block and an encoding means 4 for **variable length** encoding quantized data on an encoding side and a means for decoding **variable length** data to the quantized data, an inverse quantization means for transforming the quantized data to the DCT coefficient and an inverse DCT processing means for restoring the coefficient to the **picture** data of the block by the IDCT on a decoding side, a DCT control means 5 for storing the value of a **fixed point** arithmetic table 6 used for a DCT processing in the user area of signals to...

... encoding side and an inverse DCT control means for taking out the value of the **fixed point** arithmetic table from the user area and letting the value be used in an inverse...

12/3,K/2 (Item 2 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2005 JPO & JAPIO. All rts. reserv.

04667129 **Image available**
QUANTIZER FOR **PICTURE** COMPRESSION

PUB. NO.: 06-339029 [JP 6339029 A]
PUBLISHED: December 06, 1994 (19941206)
INVENTOR(s): SAITO HIROSHI
SAKAUCHI TATSUJI
APPLICANT(s): MATSUSHITA ELECTRIC IND CO LTD [000582] (A Japanese Company
or Corporation), JP (Japan)
APPL. NO.: 05-127142 [JP 93127142]
FILED: May 28, 1993 (19930528)

QUANTIZER FOR **PICTURE** COMPRESSION

ABSTRACT

PURPOSE: To provide a quantizer for **picture** compression in which interchangeability between orthogonal transformation and each kind of encoding standard using **variable length** encoding can be completely

maintained, and compressing efficiency can be improved...

... a quantization step is converted into an inverse number obtained as an integer by a **fixed** decimal **point** approximation by an inverse number generating means 14 by K bit round-down when the...

12/3,K/3 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

014756047 **Image available**
WPI Acc No: 2002-576751/200262
XRPX Acc No: N02-457276

Path length adjustment module for scanners has cubic mirror with
fixed **input** point

Patent Assignee: UMAX DATA SYSTEMS INC (UMAX-N)
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 20203019	U1	20020627	DE 2002U2003019	U	20020226	200262 B

Priority Applications (No Type Date): DE 2002U2003019 U 20020226

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
DE 20203019	U1		24	G02B-017/06	

Path length adjustment module for scanners has cubic mirror with
fixed **input** point

Abstract (Basic):

... A path **length** **adjustment** module has two reflecting surfaces
(B, C) arranged on a cube or other polyhedron so...

...input (a) and output (b) angles are equal and the rays always pass
through a **fixed** **point** (X1) on an imaginary mirror (A) whatever the
module position.

... Path **length** **adjustment** module for scanners, to lengthen beam
path in scanner, copier, facsimile, digital camera, video camera...

...Allows **adjustment** of path **length** by movement of the module without
affecting the position of other elements. Increases the path...

...of reflections or reflecting elements. Reduces the effect of tolerance
build up on focussing and **image** quality. Reduced weight because does
not need extra or larger elements...

... **Fixed** **point** (X1

12/3,K/4 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

012737047 **Image available**
WPI Acc No: 1999-543164/199946
Related WPI Acc No: 2000-588786; 2000-588787
XRPX Acc No: N00-417826

**Digital camera having fixed focus and automatic focus mode features - has
processor responsive to optical setting of adjusted aperture to move lens**

to focal point according to electrical signal indicative of object to be photographed

Patent Assignee: HEWLETT-PACKARD CO (HEWP); HEWLETT-PACKARD DEV CO LP (HEWP)

Inventor: DORON A

Number of Countries: 027 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 948198	A2	19991006	EP 99302103	A	19990318	199946 B
JP 2000056213	A	20000225	JP 9974686	A	19990319	200021
US 6563543	B1	20030513	US 9853819	A	19980331	200335
EP 948198	B1	20040121	EP 99302103	A	19990318	200410
DE 69914245	E	20040226	DE 614245	A	19990318	200419
			EP 99302103	A	19990318	

Priority Applications (No Type Date): US 9853819 A 19980331

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 948198	A2	E	10	H04N-005/232	
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Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI

JP 2000056213	A		7	G02B-007/28	
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US 6563543	B1			H04N-005/235	
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EP 948198	B1	E		H04N-005/232	
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Designated States (Regional): DE FR GB

DE 69914245	E			H04N-005/232	Based on patent EP 948198
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...Abstract (Basic): setting of the adjustable aperture for moving the focusable lens (14) to a determined focal point . A fixed focus mode algorithm determines a focal point based on the adjusted aperture and moves the...

...ADVANTAGE - Enables photographer to take pictures using auto focus mode yet still allows photographer to take pictures at a distance in a fixed focus mode without any auto focus time delay so that photographer doesn't have to wait to take picture . DESCRIPTION OF DRAWING - The drawing shows a diagrammatic representation of the digital camera. Camera (10); Shutter (12); Lens (14); CCD (15); Camera setting adjustment system (17); Digital signal converter (21); Processor (25); Motor (29); LCD (38...

12/3,K/5 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX

(c) 2006 Thomson Derwent. All rts. reserv.

011075425 **Image available**

WPI Acc No: 1997-053349/199706

XRPX Acc No: N97-043698

Image formation method from image points of video image - involving light beam with intensity controllable according to brightness of image points and variable focal length lens or lens system maintaining fixed focal point on object side

Patent Assignee: LDT GMBH & CO LASER-DISPLAY TECHNOLOGIE (LDTG-N); ZEISS JENA GMBH CARL (JENA); LDT GMBH & CO LASER-DISPLAY-TECHNOLOGIE (LDTL-N)

Inventor: DETER C; RODER R; ROEDER R

Number of Countries: 027 Number of Patents: 013

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 19522698	A1	19970102	DE 1022698	A	19950622	199706 B
WO 9701248	A1	19970109	WO 96EP1751	A	19960426	199708

ZA 9605308	A	19970326	ZA 965308	A	19960621	199718
EP 776575	A1	19970604	EP 96914992	A	19960426	199727
			WO 96EP1751	A	19960426	
BR 9606402	A	19971028	BR 966402	A	19960426	199750
			WO 96EP1751	A	19960426	
KR 97704295	A	19970809	WO 96EP1751	A	19960426	199836
			KR 96707098	A	19961211	
DE 19522698	C2	19980917	DE 1022698	A	19950622	199841
JP 10508958	W	19980902	WO 96EP1751	A	19960426	199845
			JP 97503535	A	19960426	
TW 341744	A	19981001	TW 96105259	A	19960502	199904
EP 776575	B1	19991229	EP 96914992	A	19960426	200005
			WO 96EP1751	A	19960426	
DE 59604035	G	20000203	DE 504035	A	19960426	200013
			EP 96914992	A	19960426	
			WO 96EP1751	A	19960426	
US 6057964	A	20000502	WO 96EP1751	A	19960426	200029
			US 97793219	A	19970219	
CN 1157081	A	19970813	CN 96190665	A	19960426	200139

Priority Applications (No Type Date): DE 1022698 A 19950622

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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DE 19522698	A1		14	G02B-026/12	
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WO 9701248	A1	G	36	H04N-009/31	
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Designated States (National): BR CA CN FI JP KR RU SI US

Designated States (Regional): AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE

ZA 9605308	A		28	G02B-000/00	
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EP 776575	A1	G	1	H04N-009/31	Based on patent WO 9701248
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Designated States (Regional): AT DE ES FI FR GB IT NL

BR 9606402	A			H04N-009/31	Based on patent WO 9701248
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KR 97704295	A			H04N-005/74	
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DE 19522698	C2			G02B-026/12	
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JP 10508958	W		30	G02B-026/10	Based on patent WO 9701248
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TW 341744	A			H04N-005/74	
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EP 776575	B1	G		H04N-009/31	Based on patent WO 9701248
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Designated States (Regional): AT DE ES FI FR GB IT NL SI

DE 59604035	G			H04N-009/31	Based on patent EP 776575
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Based on patent WO 9701248

US 6057964	A			G02B-027/10	Based on patent WO 9701248
------------	---	--	--	-------------	----------------------------

CN 1157081	A			H04N-009/31	
------------	---	--	--	-------------	--

Image formation method from image points of video image - ...

...involving light beam with intensity controllable according to brightness of image points and variable focal length lens or lens system maintaining fixed focal point on object side

...Abstract (Basic): a light beam (40) whose intensity can be controlled according to the brightness of the image points. The beam is deflected and then projected on to a screen (54) by a lens or lens system (50), whose focal length is varied to maintain a fixed focal point on the object side...

...The object side fixed focal point is maintained inside a spatially extended region (4,46), within which the deflection takes place. For a partial lens system, the fixed focal point is in a plane in the beam path of the lens system, in which the...

...ADVANTAGE - Enables **image** size to be varied in simple manner...
Title Terms: **IMAGE** ;

12/3,K/6 (Item 4 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

010801452 **Image available**
WPI Acc No: 1996-298405/199630
XRPX Acc No: N96-251259

Image data codec that uses bandwidth-compression technology - has reverse discrete-cosine-transformation controller that controls reverse DCT processing at decoding side, using value of fixed decimal point operation table

Patent Assignee: MATSUSHITA DENKI SANGYO KK (MATU)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 8130737	A	19960521	JP 94292007	A	19941102	199630 B

Priority Applications (No Type Date): JP 94292007 A 19941102

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 8130737	A		8	H04N-007/30	

Image data codec that uses bandwidth-compression technology...

...discrete-cosine-transformation controller that controls reverse DCT processing at decoding side, using value of fixed decimal point operation table

...Abstract (Basic): The decoder has a discrete cosine transformation processor (2) which performs DCT processing of an **image** data that is divided by a block to a coding side. A quantiser (3) quantises a data factor of the block. A **variable length** coding unit (4) performs **variable length** coding of the quantization data, which is then decoded in the decoding side. A reverse...

...A reverse DCT processor converts and restores the **image** data for each block. A block composition unit combines the block **image** data and the decompressed screen information. A DCT controller is set up in the processor which stores **fixed decimal point** operation table value that is used in the user processor decoding and coding side. The **fixed decimal point** operation table value is formed in the user area in the decoding side. A reverse DCT controller controls the reverse DCT processor using value of the **fixed decimal point** operation table...

...ADVANTAGE - Reduces operating error in DCT processing during **image** data expansion, thus obtaining high-quality **image** .

Title Terms: **IMAGE** ;

12/3,K/7 (Item 5 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

010585899 **Image available**
WPI Acc No: 1996-082852/199609
XRPX Acc No: N96-069227

Development appts for electrophotography copy machine - has control electrode located adjacent to insulator which is at set angle to developer roller wrt roller to photosensitive drum

Patent Assignee: KONICA CORP (KONS)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 7333977	A	19951222	JP 94121403	A	19940602	199609 B

Priority Applications (No Type Date): JP 94121403 A 19940602

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 7333977	A		12	G03G-015/08	

...Abstract (Basic): single component developer (D) to a development region (A). The development appts develops the latent **image** as the toner moves in the oscillating electric field. A control electrode (84) is located...

...as 'L1'. The projection of the insulator is taken as 'L2' measured from the terminal **point** of the **fixed** plate. The length 'L3' is taken as the distance between the farther end of the control electrode and the hidden end of the insulator. These **lengths** are **adjusted** so that $L1 + L2 = 0.8 * L3$ and $L1:L2 = 2:1 - 1:20$...

...ADVANTAGE - Prevents generation of **image** strain. Stabilises development process...

12/3,K/8 (Item 6 from file: 350)

DIALOG(R)File 350:Derwent WPIX

(c) 2006 Thomson Derwent. All rts. reserv.

010459117 **Image available**

WPI Acc No: 1995-360436/199547

XRPX Acc No: N95-267965

Colour image area transverse registration for photocopier belt - detecting lateral position of belt at defined points along its length and adjusting position in response

Patent Assignee: XEROX CORP (XERO)

Inventor: BARES J; CASTELLI V R; DEJONG J N M

Number of Countries: 004 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 679018	A2	19951025	EP 95302514	A	19950413	199547 B
US 5510877	A	19960423	US 94230469	A	19940420	199622
EP 679018	A3	19960529	EP 95302514	A	19950413	199632
EP 679018	B1	19991201	EP 95302514	A	19950413	200001
DE 69513557	E	20000105	DE 613557	A	19950413	200009
			EP 95302514	A	19950413	

Priority Applications (No Type Date): US 94230469 A 19940420

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 679018	A2	E	20	H04N-001/50	
					Designated States (Regional): DE FR GB
EP 679018	B1	E		H04N-001/50	
					Designated States (Regional): DE FR GB
DE 69513557	E			H04N-001/50	Based on patent EP 679018
US 5510877	A		19	G03G-021/00	
EP 679018	A3			H04N-001/50	

Colour image area transverse registration for photocopier belt...

...detecting lateral position of belt at defined points along its length and adjusting position in response

...Abstract (Basic): belt or to a lateral position of an edge of the belt relative to a **fixed point** . A lateral position of the belt during movement is detected by measuring a lateral position...

...The transverse location of the **image** area on the belt is adjusted to compensate for a difference between the detected position...

...Abstract (Equivalent): A method for transverse registration of an **image** area to be exposed on a longitudinally moving belt subject to lateral deviation from linear...

...ii) a lateral position of an edge of the belt with respect to a spatially **fixed** reference point ;

...

...adjusting the transverse location of the **image** area on the belt by shifting an active scan line to compensate for a difference

...Title Terms: **IMAGE** ;

12/3,K/9 (Item 7 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
 (c) 2006 Thomson Derwent. All rts. reserv.

008189682 **Image available**
 WPI Acc No: 1990-076683/199011
 XRAM Acc No: C90-033520
 XRPX Acc No: N90-058929

Forming score lines in packaging material web - by local evaporation using variable intensity laser beam

Patent Assignee: LPF VERPAKKINGEN BV (LPFV-N); LEEUWARDER
 PAPIERWARENFABRIEK BV (LEEUN-N); LEEUWARDER PAPIERWA (LEEUN-N)
 Inventor: HUIZINGA G J
 Number of Countries: 015 Number of Patents: 006
 Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 357841	A	19900314	EP 88201929	A	19880907	199011 B
US 5001325	A	19910319	US 89400934	A	19890831	199114
CA 1324420	C	19931116	CA 610280	A	19890905	199401
EP 357841	B1	19940302	EP 88201929	A	19880907	199409
DE 3888179	G	19940407	DE 3888179	A	19880907	199415
			EP 88201929	A	19880907	
ES 2051829	T3	19940701	EP 88201929	A	19880907	199429

Priority Applications (No Type Date): EP 88201929 A 19880907

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 357841	A	E	12		
					Designated States (Regional): AT BE CH DE ES FR GB GR IT LI LU NL SE
EP 357841	B1	E	11	B29C-053/06	
					Designated States (Regional): AT BE CH DE ES FR GB GR IT LI LU NL SE
DE 3888179	G			B29C-053/06	Based on patent EP 357841
ES 2051829	T3			B29C-053/06	Based on patent EP 357841
CA 1324420	C			B29C-053/06	

...Abstract (Equivalent): which is movable relative to said packaging material in two mutually perpendicular directions from a **fixed point** in response to control signals, which define the path of the score line to be...

...first side, thereby forming score lines thereon, said web having a recurring pattern of printed **images** appearing on one side thereof; moving said first laser beam in response to pattern dependent...

...provided in registration with said pattern and being responsive to the speed of the web; **adjusting** the focal **length** of said laser beam so that the focus of the first laser beam is maintained...

...score lines are being formed thereon; monitoring positional irregularities in the recurring pattern of printed **images** on said one side of the web; and adjusting in response to said monitoring, the...

12/3,K/10 (Item 8 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

003035328

WPI Acc No: 1981-D5342D/198116

Zoom objective with mechanically compensated fixed image plane - has movable single lenses and fixed lens groups with aspherical surfaces

Patent Assignee: PHILIPS GLOEILAMPENFAB NV (PHIG)

Inventor: HUGUES E A

Number of Countries: 009 Number of Patents: 011

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
NL 8005339	A	19810331				198116 B
DE 3035744	A	19810416				198117
GB 2062280	A	19810520	GB 8030829	A	19800924	198121
SE 8006688	A	19810504				198121
FR 2466785	A	19810430				198125
CA 1150544	A	19830726				198333
US 4416518	A	19831122				198349
GB 2062280	B	19840229				198409
CH 655802	A	19860515				198625
SE 450917	B	19870810				198734
IT 1132751	B	19860702				198751

Priority Applications (No Type Date): FR 7924154 A 19790928

Zoom objective with mechanically compensated fixed image plane...

...Abstract (Basic): The objective with **variable** focal **length** and fixed **image** plane (20) consists of lens groups of which at least two (4,5,6) contain...

...lenses, whether spherical or not, is less than in any other zoom objective with fixed **image** plane having spherical lenses only...

...For a zoom ratio greater than five, the **image** plane is held at a **fixed point** by mechanical compensation. All movable groups consist of single lenses of which at least one...

...Abstract (Equivalent): A variable-focus objective with stationary **image** plane, comprising groups of lenses, at least two of said groups including a lens with...

...focal-length variation is at least 5 to 1, that a stationary position of

the **image** plane during a focal length variation is obtained only through mechanical compensation as defined herein...
...Title Terms: **IMAGE** ;

12/3,K/11 (Item 9 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

002065657

WPI Acc No: 1978-78725A/197844

**Appts. for adjusting focal length of solar energy reflector mirrors -
has mechanical means for altering shape of reflective surface**

Patent Assignee: ELECTRICITE DE FRANCE (ELEC)

Inventor: DESSUS B; VENTRE E

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
FR 2379078	A	19780929				197844 B

Priority Applications (No Type Date): FR 772462 A 19770128

Appts. for adjusting focal length of solar energy reflector mirrors
...

...Abstract (Basic): Each mirror of the assembly is designed to form an **image** of aluminous source on a **fixed point** . The luminous source is at an infinitely large distance from the mirrors, e.g. the sun. The appts. **adjusts** the focal **length** of the mirrors by adjusting the shape of their reflecting surface. It is done by...

12/3,K/12 (Item 10 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

001538342

WPI Acc No: 1976-L1285X/197646

**Automatic focussing attachment for photographic enlargers - has pivoted
link between focussing knob and fixed support**

Patent Assignee: CRAIG D R (CRAI-I)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 3989373	A	19761102				197646 B

Priority Applications (No Type Date): US 74478769 A 19740612

...Abstract (Basic): the lens as the enlarger is raised and lowered to vary the size of the **image** . A pivoted link of **variable effective length** has one end connected to a **point fixed** with respect to the base of the enlarger, and the opposite end portion connected to...

...The **variable effective length** link has several forms including a number of pivoted leaves and a sliding telescoping mounting.

15/3,K/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

016936736 **Image available**
WPI Acc No: 2005-261046/200527
XRPX Acc No: N05-214323

Graphics rendering method for embedded device e.g. mobile computing device, involves processing rendering data in variable - length fixed - point format, and rendering processed rendering data on embedded device

Patent Assignee: MICROSOFT CORP (MICT)
Inventor: BUCKMAN J W; DENG K; GUO B; WANG L
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20050062762	A1	20050324	US 2003661055	A	20030913	200527 B

Priority Applications (No Type Date): US 2003661055 A 20030913

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20050062762	A1		28	G09G-005/00	

... rendering method for embedded device e.g. mobile computing device, involves processing rendering data in variable - length fixed - point format, and rendering processed rendering data on embedded device

Abstract (Basic):

... rendering data in one format. The rendering data from the format is converted into a **variable length fixed - point** format. The rendering data in the **variable - length fixed - point** format is processed. The processed rendering data is rendered on an embedded device. A mathematical library is created for processing the rendering data in the **variable - length fixed - point** format.

... The data is rendered in the **variable - length fixed - point** format, thus enabling efficient and fast graphics rendering in the embedded devices. The graphics rendering...

...The drawing shows an implementation of normalized vectors in a **Direct3D** mobile phone model...

?

RUSH SPE SIGNATURE _____

Access DB#

17585746

SEARCH REQUEST FORM
Scientific and Technical Information Center

EIC 2600

Requester's Full Name Reyan Yang Examiner # 76963 Date 1/6/06
Art Unit 2672 Phone Number 2-7666 Serial Number 101661,055
Office Location KNX16A19 Format preferred (circle) PAPER EMAIL BOTH

If more than one search is submitted, please prioritize searches in order of need.
.....

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Let us know what you already have and so do not need. Include the keywords, synonyms and meaning of acronyms. Define all terms that may have a specific meaning. Please attach a copy of the background, abstract, claims and other pertinent information.

Please state how the terms or keyword strings should relate to one another.

Title of the Invention _____

Inventor(s) _____

Earliest Priority date to be used _____

converting a format to fixed point format
rendering graphics.

STAFF USE ONLY

Searcher Samir Patel

Phone 2-3537

Location KNX-886

Date picked up 01/06/05 / 11:20am

Date completed 01/06/05 / 2:30pm

Search Prep/review 40

Online Time 120

TYPE of Search

Text ☒

Litigation ☐

Other ☐

Databases Searched

Dialog ☒

STN ☐

QuestelOrbit ☐

LEXIS/NEXIS ☐

Courtlink ☐

Other ☐



STIC Search Results Feedback Form

EIC 2600

Questions about the scope or the results of the search? Contact *the EIC searcher* or contact:

Pamela Reynolds, EIC 2600 Team Leader
571-272-3505, Knox 8B59

Voluntary Results Feedback Form

➤ I am an examiner in Workgroup: Example: 2663

➤ Relevant prior art **found**, search results used as follows:

- ☐ 102 rejection
- ☐ 103 rejection
- ☐ Cited as being of interest.
- ☐ Helped examiner better understand the invention.
- ☐ Helped examiner better understand the state of the art in their technology.

Types of relevant prior art found:

- ☐ Foreign Patent(s)
- ☐ Non-Patent Literature
(journal articles, conference proceedings, new product announcements etc.)

➤ Relevant prior art **not found**:

- ☐ Results verified the lack of relevant prior art (helped determine patentability).
- ☐ Results were not useful in determining patentability or understanding the invention.

Comments:

Drop off or send completed forms to STIC/EIC2600 Knox 8B59



File 2:INSPEC 1898-2005/Dec W2
(c) 2005 Institution of Electrical Engineers
File 6:NTIS 1964-2005/Dec W2
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File 8:Ei Compendex(R) 1970-2006/Dec W4
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File 27:Foundation Grants Index 1990-2005/Dec
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(c) 2006 Inst for Sci Info
File 35:Dissertation Abs Online 1861-2005/Dec
(c) 2005 ProQuest Info&Learning
File 62:SPIN(R) 1975-2006/Oct W5
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(c) 1999 Information Handling Services
File 94:JICST-EPlus 1985-2006/Oct W5
(c)2006 Japan Science and Tech Corp(JST)
File 95:TEME-Technology & Management 1989-2006/Jan W1
(c) 2006 FIZ TECHNIK
File 99:Wilson Appl. Sci & Tech Abs 1983-2005/Oct
(c) 2005 The HW Wilson Co.
File 144:Pascal 1973-2005/Dec W2
(c) 2005 INIST/CNRS
File 239:Mathsci 1940-2005/Feb
(c) 2005 American Mathematical Society
File 434:SciSearch(R) Cited Ref Sci 1974-1989/Dec
(c) 1998 Inst for Sci Info
File 583:Gale Group Globalbase(TM) 1986-2002/Dec 13
(c) 2002 The Gale Group
File 603:Newspaper Abstracts 1984-1988
(c)2001 ProQuest Info&Learning
File 483:Newspaper Abs Daily 1986-2006/Jan 04
(c) 2006 ProQuest Info&Learning
File 248:PIRA 1975-2006/Dec W3
(c) 2006 Pira International
File 56:Computer and Information Systems Abstracts 1966-2005/Dec
(c) 2005 CSA.
File 57:Electronics & Communications Abstracts 1966-2005/Dec
(c) 2005 CSA.
File 61:Civil Engineering Abstracts. 1966-2005/Dec
(c) 2005 CSA.

Set	Items	Description
S1	721818	(CONVERT????? OR CONVER??????? OR TRANSFORM????? OR CHANG?- ??? OR TRANSLAT????)(7N)(FORMAT?? OR DESIGN?? OR STRUCTU???? - OR ARRANGE????? OR PATTERN????)
S2	111967	FIX??(2N)POINT??
S3	10032	S2(7N)(FORMAT?? OR DESIGN?? OR STRUCTU???? OR ARRANGE????? OR PATTERN????)
S4	3319526	IMAG?? OR GRAPHIC??? OR PICTURE??
S5	620738	(RENDER???? OR PROCESS????)(5N)S4
S6	56536	AU=(WANG L? OR WANG, L? OR DENG K? OR DENG, K? OR GUO B? OR GUO, B? OR BUCKMAN J? OR BUCKMAN, J?)
S7	255	S1 AND S3
S8	25	S7 AND S4
S9	17	RD (unique items)
S10	16	S9 NOT PY>2003
S11	1	S6 AND S1 AND S3

10/3,K/1 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

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08992163 INSPEC Abstract Number: B2004-07-1265F-042, C2004-07-5130-029

Title: Arithmetic processing unit for reciprocal operations

Author(s): Rounioja, K.; Parviainen, J.A.

Author Affiliation: Nokia Mobile Phones, Oulu, Finland

Conference Title: Proceedings. 2003 International Symposium on System-on-Chip (IEEE Cat. No.03EX748) p.109-12

Editor(s): Nurmi, J.; Takala, J.; Hamalainen, T.D.

Publisher: IEEE, Piscataway, NJ, USA

Publication Date: 2003 Country of Publication: USA xxii+170 pp.

ISBN: 0 7803 8160 2 Material Identity Number: XX-2003-02810

U.S. Copyright Clearance Center Code: 0-7803-8160-2/03/\$17.00

Conference Title: Proceedings. 2003 International Symposium on System-on-Chip

Conference Sponsor: Nokia

Conference Date: 19-21 Nov. 2003 Conference Location: Tampere, Finland

Language: Chinese

Subfile: B C

Copyright 2004, IEE

...Abstract: has been developed. The method utilizes look up tables that are compressed by normalizing the **fixed** point operand to floating point **format**, using symmetric table addition method and **converting** value back to **fixed point format**. Developed method enables significant speed-ups in **graphics** processing.

Descriptors: computer **graphics** ;

...Identifiers: **graphics** processing

10/3,K/2 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

08462566 INSPEC Abstract Number: C2003-01-5230-009

Title: A library of parameterized floating-point modules and their use

Author(s): Belanovic, P.; Leeser, M.

Author Affiliation: Dept. of Electr. & Comput. Eng., Northeastern Univ., Boston, MA, USA

Conference Title: Field-Programmable Logic and Applications. Reconfigurable Computing Is Going Mainstream. 12th International Conference, FPL 2002. Proceedings (Lecture Notes in Computer Science Vol.2438) p.657-66

Editor(s): Glesner, M.; Zipf, P.; Renovell, M.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 2002 Country of Publication: Germany xxii+1187 pp.

ISBN: 3 540 44108 5 Material Identity Number: XX-2002-02825

Conference Title: Field-Programmable Logic and Applications. Reconfigurable Computing Is Going Mainstream. 12th International Conference, FPL 2002. Proceedings

Conference Date: 2-4 Sept. 2002 Conference Location: Montpellier, France

Language: English

Subfile: C

Copyright 2002, IEE

...Abstract: formats for reconfigurable hardware. We have developed a library of fully parameterized hardware modules for **format** control, arithmetic operations and **conversion** to and from any **fixed - point**

format . The **format converters** allow for hybrid implementations that combine both **fixed** and floating-**point** calculations. This permits the **designer** to choose between the increased range of floating-point and the increased precision of fixed...

...library with a hybrid implementation of the K-means clustering algorithm applied to multispectral satellite **images** .

...Identifiers: multispectral satellite **images**

10/3,K/3 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

07403545 INSPEC Abstract Number: C1999-12-5230-010

Title: Automatic floating to fixed point translation and its application to post-rendering 3D warping

Author(s): Leong, M.P.; Yeung, M.Y.; Yeung, C.K.; Fu, C.W.; Heng, P.A.; Leong, P.H.W.

Author Affiliation: Dept. of Comput. Sci. & Eng., Chinese Univ. of Hong Kong, Shatin, Hong Kong

Conference Title: Seventh Annual IEEE Symposium on Field-Programmable Custom Computing Machines (Cat. No.PR00375) p.240-8

Editor(s): Pocek, K.L.; Arnold, J.M.

Publisher: IEEE Comput. Soc, Los Alamitos, CA, USA

Publication Date: 1999 Country of Publication: USA x+319 pp.

ISBN: 0 7695 0375 6 Material Identity Number: XX-1999-02920

U.S. Copyright Clearance Center Code: 0 7695 0375 6/99/\$10.00

Conference Title: Seventh Annual IEEE Symposium on Field-Programmable Custom Computing Machines

Conference Sponsor: IEEE Comput. Soc. Tech. Comm. on Comput. Archit

Conference Date: 21-23 April 1999 Conference Location: Napa Valley, CA, USA

Language: English

Subfile: C

Copyright 1999, IEE

...Abstract: can be efficiently implemented in an FCCM remains an obstacle in the rapid systems prototyping **design** flow. Floating **point** to **fixed point conversion** is tedious, error prone and requires a good knowledge of fixed point computer arithmetic. This...

... the information collected by the system and outputs synthesisable VHDL code. A post-rendering 3D **image** warping application designed using this system is used as an example.

...Descriptors: **image** processing

...Identifiers: post-rendering 3D **image** warping...

10/3,K/4 (Item 4 from file: 2)

DIALOG(R)File 2:INSPEC

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06610773 INSPEC Abstract Number: B9708-6130-007, C9708-5260S-002

Title: On the architectural support for fast wavelet transform

Author(s): Chang, C.C.; Liu, J.-C.; Chan, A.K.

Author Affiliation: Dept. of Electr. Eng., Texas A&M Univ., College Station, TX, USA

Journal: Proceedings of the SPIE - The International Society for Optical Engineering Conference Title: Proc. SPIE - Int. Soc. Opt. Eng. (USA)

vol.3078 p.700-7

Publisher: SPIE-Int. Soc. Opt. Eng,

Publication Date: 1997 Country of Publication: USA

CODEN: PSISDG ISSN: 0277-786X

SICI: 0277-786X(1997)3078L:700:ASFW;1-R

Material Identity Number: C574-97104

U.S. Copyright Clearance Center Code: 0 8194 2493 5/97/\$10.00

Conference Title: Wavelet Applications OV

Conference Sponsor: SPIE

Conference Date: 22-24 April 1997 Conference Location: Orlando, FL, USA

Language: English

Subfile: B C

Copyright 1997, IEE

Abstract: The wavelet transform is being used for many real-time signal and **image** processing applications. Many applications can tolerate a certain level of compromise in accuracy for a faster speed. We propose a wavelet processor architecture to support approximated calculation of the wavelet **transform**. Our **design** uses the **fixed point** number system to simplify the hardware **design** and the computation procedures. By using a table look-up technique, one can predict the...

...Descriptors: **image** processing

...Identifiers: real-time **image** processing...

10/3,K/5 (Item 5 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

05495172 INSPEC Abstract Number: B9311-6140C-142, C9311-1250-097

Title: Solution of morphological operator relations with invariance boundary conditions

Author(s): Dougherty, E.R.

Author Affiliation: Center for Imaging Sci., Rochester Inst. of Technol., NY, USA

Journal: Proceedings of the SPIE - The International Society for Optical Engineering vol.2030 p.33-42

Publication Date: 1993 Country of Publication: USA

CODEN: PSISDG ISSN: 0277-786X

U.S. Copyright Clearance Center Code: 0 8194 1105 1/93/\$6.00

Conference Title: Diagnostic and Therapeutic Cardiovascular Interventions III

Conference Sponsor: SPIE; Biomed. Opt. Soc

Conference Date: 16-17 Jan. 1993 Conference Location: Los Angeles, CA, USA

Language: English

Subfile: B C

Abstract: The paper formulates increasing, **translation** -invariant filter **design** for the subtractive-noise restoration problem in terms of the classical boundary-value-problem paradigm. The boundary-value problem involves both operator relations and invariant (**fixed - point**) boundary conditions. A **design** approach is formulated that derives the morphological basis expansion directly from the statement of the...

...Descriptors: **image** processing

Identifiers: **image** analysis...

... **translation** -invariant filter **design** ;

10/3,K/6 (Item 6 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2005 Institution of Electrical Engineers. All rts. reserv.

04257480 INSPEC Abstract Number: B88070834, C88062750
Title: Design and implementation of a two-dimensional fast Fourier transform chip
Author(s): Krakow, W.T.; Batchelor, W.E.; Liu, W.; Hildebrandt, T.; Hughes, T.; Yeh, T.-F.; Salama, R.; Mei, G.
Author Affiliation: Microelectron. Center of North Carolina, Research Triangle Park, NC, USA
Conference Title: Proceedings of the IEEE 1988 Custom Integrated Circuits Conference (Cat. No.88CH2584-1) p.8.4/1-4
Publisher: IEEE, New York, NY, USA
Publication Date: 1988 Country of Publication: USA 816 pp.
U.S. Copyright Clearance Center Code: CH2584-1/88/0000-0042\$01.00
Conference Sponsor: IEEE
Conference Date: 16-19 May 1988 Conference Location: Rochester, NY, USA
Language: English
Subfile: B C

...Abstract: description is given of a rasterized, pipelined architecture for performing a two-dimensional fast Fourier **transformation** (2DFFT). A chip has been **designed** for implementing this architecture in 1.25- μ m CMOS. Each chip consist of 152...

...chips operate at a clock speed of 10 MHz and process a 256*256-pixel **image** at a real-time rate of 30 Hz. Each chip has input and output data **formats** consisting of rasterized streams of 22-bit **fixed - point** complex numbers. The authors present the chip architecture and describe the design of its constituent...

...Descriptors: computerised **picture** processing...

10/3,K/7 (Item 1 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)
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06091268 E.I. No: EIP02287015280
Title: Floating-point bit-width optimization for low-power signal processing applications
Author: Fang, Fang; Chen, Tsuhan; Rutenbar, Rob A.
Corporate Source: Dept. of Elec. and Comp. Engineering Carnegie Mellon University, Pittsburgh, PA 15213, United States
Conference Title: 2002 IEEE International Conference on Acoustic, Speech, and Signal Processing
Conference Location: Orlando, FL, United States Conference Date: 20020513-20020517
E.I. Conference No.: 59257
Source: ICASSP, IEEE International Conference on Acoustics, Speech and Signal Processing - Proceedings v 3 2002. p III/3208-III/3211 (IEEE cat n 02ch37334)
Publication Year: 2002
CODEN: IPRODJ ISSN: 0736-7791
Language: English

...Abstract: optimization helps to reduce the cost further. We apply the proposed design flow to the **design** of inverse discrete cosine **transform** (IDCT), and show that the power consumption of our lightweight FP IDCT is

comparable to an optimized **fixed - point design** . In addition, promising results on some real-world applications such as video coding and speech...

...Descriptors: radio systems; Search engines; Computer hardware; Numerical methods; Computational complexity; Algorithms; Cosine transforms; Computer simulation; **Image** coding; Speech recognition

10/3,K/8 (Item 2 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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05057033 E.I. No: EIP97093813179

Title: Architectural support for fast wavelet transform

Author: Chang, Charles C.; Liu, Jyh-Charn S.; Chan, Andrew K.

Corporate Source: Texas A&M Univ., Coll. Station, TX, USA

Conference Title: Wavelet Applications IV

Conference Location: Orlando, FL, USA Conference Date: 19970422-19970424

E.I. Conference No.: 23060

Source: Proceedings of SPIE - The International Society for Optical Engineering v 3078 1997. Society of Photo-Optical Instrumentation Engineers, Bellingham, WA, USA. p 700-707

Publication Year: 1997

CODEN: PSISDG ISSN: 0277-786X ISBN: 0-8194-2493-5

Language: English

Abstract: Wavelet transform is being used for many real-time signal and **image** processing applications. Many applications can tolerate certain level of compromise in accuracy for a faster...

...this paper we propose a wavelet processor architecture to support approximated calculation of the wavelet **transform** .Our **design** uses the **fixed point** number system to simplify the hardware **design** and the computation procedures. By using a table look-up technique, one can predict the...

Descriptors: ***Image** processing; Wavelet transforms; Real time systems; Computer simulation; Speech processing; Signal processing; Design; Table lookup

10/3,K/9 (Item 3 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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03037183 E.I. Monthly No: EIM9103-011099

Title: Roundoff noise of the cascade structure implementation for the filters designed by McClellan transformations.

Author: Lien, Brian K.

Corporate Source: Fu Jen Univ, Taiwan

Conference Title: Image Processing Algorithms and Techniques

Conference Location: Santa Clara, CA, USA Conference Date: 19900212

E.I. Conference No.: 13863

Source: Proceedings of SPIE - The International Society for Optical Engineering v 1244. Publ by Int Soc for Optical Engineering, Bellingham, WA, USA. p 45-54

Publication Year: 1990

CODEN: PSISDG ISSN: 0277-786X ISBN: 0-8194-0291-5

Language: English

Abstract: Because of the easiness of **design** and efficiency of Implementation, the McClellan **transform** is by far the most popular and successful method to design multi-dimensional finite impulse response filters for **Image** Processing. The filters can be implemented efficiently by the directed structure, cascade structure and Chebyshev...
...sensitivity and inherent pipelinability. Also it is very suitable to be implemented by nowadays modular **image** processing hardware. So with the output roundoff noise reduced, the cascade structure will prove itself to be a suitable **structure** for the **fixed - point** implementation of the filters **designed** by McClellan transformations. (Author abstract) 13 Refs.

...Descriptors: Design; **IMAGE** PROCESSING; NOISE, SPURIOUS SIGNAL; MATHEMATICAL TRANSFORMATIONS

10/3,K/10 (Item 1 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online
(c) 2005 ProQuest Info&Learning. All rts. reserv.

01863536 ORDER NO: AADAA-INQ64894
Axiomatic approach to the modeling of product conceptual design processes using set theory

Author: Zeng, Yong

Degree: Ph.D.

Year: 2001

Corporate Source/Institution: University of Calgary (Canada) (0026)

Source: VOLUME 62/12-B OF DISSERTATION ABSTRACTS INTERNATIONAL.

PAGE 5931. 215 PAGES

ISBN: 0-612-64894-X

...is not perfect while the axiom of object structuring indicates what should be a full **picture** of an object. These two axioms deal with human and natural parts in the design...

...structured nature of design problem, is obtained from the axiomatic system. This equation implies that **design** problem solving is a process looking for **fixed points** under the **design** function, which is nonlinear in nature. This associates designing to nonlinear dynamics and leads to...

...developed based on the principles implied in the established theory. The software automatically generates multiple **design** concepts for **changing** straight-line motions merely with the performance knowledge. This software prototype demonstrates the usefulness of...

10/3,K/11 (Item 2 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online
(c) 2005 ProQuest Info&Learning. All rts. reserv.

01850968 ORDER NO: AADAA-I3025869
FPGA design methodologies for high-performance applications

Author: Leong, Monk Ping

Degree: Ph.D.

Year: 2001

Corporate Source/Institution: Chinese University of Hong Kong (People's Republic of China) (1307)

Source: VOLUME 62/09-B OF DISSERTATION ABSTRACTS INTERNATIONAL.

PAGE 4083. 278 PAGES

ISBN: 0-493-37841-3

...design are predominately treated as different entities, tools for

developers not intimately familiar with hardware **design** to **translate** a software implementation to hardware can greatly improve productivity. Secondly, resources on an FPGA device...

...the two entities need to be addressed.

In this dissertation, a high level FPGA coprocessor **design** system which can automatically **translate** a high level floating-point algorithmic description into an optimized FPGA hardware/software co-design system was developed. This system utilizes two commonly used but seldom simultaneously applied **design** methodologies, namely floating to **fixed - point conversion** and digit-serial computation. The system takes a floating-point dataflow algorithmic description and **translates** it into a **fixed - point design** via a simulation-based optimization. The optimizer assigns a wordlength and digit size to each...

...the standard peripheral bus.

The above techniques were applied to a number of applications in **image** processing, cryptography, rendering and auditory signal processing. In each application, the approach was shown to...

10/3,K/12 (Item 3 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online
(c) 2005 ProQuest Info&Learning. All rts. reserv.

01738649 ORDER NO: AADAA-I9963887

The impact of computer architecture features on image processing application execution times: A case study using MPEG image sequence compression on the IBM SP2

Author: Salinger, Jeremy Alan

Degree: Ph.D.

Year: 2000

Corporate Source/Institution: The University of Michigan (0127)

Source: VOLUME 61/03-B OF DISSERTATION ABSTRACTS INTERNATIONAL.

PAGE 1496. 146 PAGES

The impact of computer architecture features on image processing application execution times: A case study using MPEG image sequence compression on the IBM SP2

Many **image** -processing applications require special-purpose hardware to run in real time. Others can run on general-purpose computers only on small **images** . The use of **image** -processing programs becomes more common as general-purpose computers advance so they run these programs in real time on larger **images** .

The field of mathematics called **image** algebra defines a complete set of data and operations for expressing any **image** -to- **image** transformation. This mathematical foundation has previously been used to guide the development of special-purpose **image** processors. The research reported in this thesis expands on the previous work to show how...

...that could improve the performance of general-purpose computers when executing a wide variety of **image** -processing programs.

We used an MPEG2 encoding program as a benchmark to test the validity ...

...suggested, and the experiments verified, specific enhancements to decrease the execution time for the fundamental **image** -algebra operations. The most significant impact would be to increase the number of moderate precision...

...executed each clock cycle. This can be accomplished with additional fixed-point units, instructions to **convert** from **fixed - point** to floating-point **format** , or floating-point units that can operate on fixed-point data. Compound instructions for integer multiply-accumulate, add-max, and multiply-max are also recommended to better support the fundamental **image** -algebra operations.

The theoretical background was also applied to identify and evaluate similar improvements to...

...identified to improve the ability of message-passing distributed-memory MIMD computers to execute parallelized **image** -processing programs.

We concluded that the **image** algebra provides a solid theoretical foundation that suggests effective ways to improve general-purpose computers so they execute **image** -processing programs faster.

10/3,K/13 (Item 1 from file: 144)
DIALOG(R)File 144:Pascal
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16485060 PASCAL No.: 04-0129208
High performance low power video compression techniques for future digital video solution
MWSCAS - 2002 : Midwest symposium on circuits and systems : Tulsa OK, 4-7 August 2002
WANALERTLAK Weetit; LEE Chee; SHIUE Wen-Tsong
Department of Electrical and Computer Engineering, Oregon State University, Owen 220, Corvallis, OR 97331, United States
IEEE Circuits and Systems Society, United States; Oklahoma State University. School of Electrical and Computer Engineering, United States
Midwest symposium on circuits and systems, 45 (Tulsa OK USA) 2002-08-04
2002 vol2, 5-8
Publisher: IEEE, Piscataway NJ
Language: English

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...the timing, memory storage and power dissipation such that the quality of video and/or **image** is still maintained. Our contributions in this paper include (i) software codec techniques such as...

... hardware design procedures. Our benchmark results show that for an intra-frame with a 352x288 **image** size, 3157 frames can be processed in 22.23 seconds - almost 6x faster than the...

English Descriptors: Performance evaluation; Low-power electronics; Video technique; **Image** compression; Energy dissipation; Software; Codec; **Fixed** point; Discrete cosine **transforms** ; Quad tree; Tree **structure** ; Data **structure** ; Algorithm; Computational complexity; Computer hardware; Compiler; Low voltage; Microprocessor; Power consumption; Integrated circuit

French Descriptors: Evaluation performance; Electronique faible puissance; Technique video; Compression **image** ; Dissipation energie; Logiciel; Codec; Virgule fixe; **Transformation** cosinus discrete; Quad arbre; **Structure** arborescente; **Structure** donnee; Algorithme; Complexite calcul; Materiel(informatique); Compileur; Basse tension; Microprocesseur; Consommation energie electrique; Circuit integre

Spanish Descriptors: Evaluacion prestacion; Tecnica video; Compresion

imagen ; Disipacion energia; Programa; Codec; Coma fija; Quad arbol;
Estructura arborescente; Estructura datos; Algoritmo; Complejidad
computacion...

10/3,K/14 (Item 2 from file: 144)
DIALOG(R)File 144:Pascal
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16115751 PASCAL No.: 03-0274375
**A variable-radix digit-serial design methodology and its application to
the discrete cosine transform**
LEONG M P; LEONG Philip H W
Department of Computer Science and Engineering, The Chinese University of
Hong Kong, Shatin, N.T., Hong Kong
Journal: IEEE transactions on very large scale integration (VLSI) systems
, 2003, 11 (1) 90-104
Language: English

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... variable-radix digit-serial design methodology and its application to
the implementation of a systolic **structure** for computing the discrete
cosine **transform** is presented. Based on the parameters supplied by a
user, different **fixed - point designs** can be derived from a single
floating-point description where tradeoffs among quantization effects,
throughput...

English Descriptors: Circuit **design** ; Implementation; Field programmable
gate array; Discrete cosine **transforms** ; Fixed point; **Image** processing

French Descriptors: Conception circuit; Implementation; Reseau porte
programmable; Transformation cosinus discrete; Virgule fixe; Traitement
image

Spanish Descriptors: Diseno circuito; Ejecucion; Red puerta programable;
Coma fija; Procesamiento **imagen**

10/3,K/15 (Item 3 from file: 144)
DIALOG(R)File 144:Pascal
(c) 2005 INIST/CNRS. All rts. reserv.

12926365 PASCAL No.: 97-0195829
Short-term persistence of low cloud features. Discussion. Author's reply
Remote sensing : a valuable source of information : Toulouse, 22-25 April
1996

La teledetection - source precieuse de renseignements
HELVEY R A; ANDERSON K comment; PHILBRICK C R comment
Geophysics Branch, Code 521420E, Naval Air Warfare Center, Weapons
Division, Point Mugu, CA 93042-5001, United States
Remote sensing : a valuable source of information. Symposium (Toulouse
FRA) 1996-04-22
Journal: AGARD Conference Proceedings, 1996 (582) 30.1-30.4
Language: English

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Considerable structure is present in low cloud features seen in weather
satellite **images** of the region offshore southern California. From

geostationary imagery, it is evident that **translation** of this **structure** is largely responsible for the variability in cloudiness experienced at any fixed point. This report...

... of cloud features are obtained, to assess the possible value of extrapolation of pre-existing **patterns** in forecasting conditions at any **fixed point** for a few hours.

10/3,K/16 (Item 1 from file: 239)
DIALOG(R)File 239:Mathsci
(c) 2005 American Mathematical Society. All rts. reserv.

02012370 MR 88b#55003c
Teoria de punto fijo. Vol III.
Fixed-point theory. Vol. III
Translated from the German by Carlos Prieto.
Dold, Albrecht
Contributors: Prieto, Carlos
Publ: Universidad Nacional Autonoma de Mexico, Mexico City,
1986, vi+188 pp.
Series: Monografias del Instituto de Matematicas [Monographs of the
Institute of Mathematics], 18.
Language: Spanish
Subfile: MR (Mathematical Reviews) AMS
Abstract Length: LONG (93 lines)
Reviewer: Brown, Robert Freeman (1-UCLA)

...the reader. The material is attractive and of broad appeal; surely
some enterprising publisher can **arrange** for an English **translation** !
The notes begin with a survey of the topics from differential topology
that the reader...

...that the domain be bounded is removed in favor of requiring only that
the inverse **image** of Y be compact (but Y is unrestricted otherwise).
After establishing the many necessary properties...

Descriptors: ...; General topology (For the topology of manifolds of all
dimensions, see 57Nxx)-Connections with other **structures**, applications-
Fixed - point and coincidence theorems (See also 47H10, 55M20...
?

11/3,K/1 (Item 1 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2005 Institution of Electrical Engineers. All rts. reserv.

08875982 INSPEC Abstract Number: C2004-04-1230D-025

Title: On discrete N-layer heteroassociative memory models

Author(s): Waivio, R.

Author Affiliation: Dept. of Comput. Sci., Illinois Univ., Chicago, IL, USA

Conference Title: ICONIP '02. Proceedings of the 9th International Conference on Neural Information Processing. Computational Intelligence for the E-Age (IEEE Cat. No.02EX575) Part vol.1 p.60-4 vol.1

Editor(s): Wang, L.; Rajapakse, J.C.; Fukushima, K.; Lee, S-Y.; Yao, X.

Publisher: Nanyang Technol. Univ, Singapore

Publication Date: 2002 Country of Publication: Singapore 5 vol.xlix+2687 pp.

ISBN: 981 04 7524 1 Material Identity Number: XX-2002-03291

Conference Title: 9th International Conference on Neural Information Processing

Conference Sponsor: Asia-Pacific Neural Network Assembly; Singapore Neuroscience Assoc.; SEAL & FSKD Conference Steering Committees; IEEE Neural Networks Soc.; Int. Neural Network Soc.; Eur. Neural Network Soc.; SPIE

Conference Date: 18-22 Nov. 2002 Conference Location: Singapore

Language: English

Subfile: C

Copyright 2004, IEE

...Abstract: reconstructed. Following are some of the main contributions of this paper: - We show how to **transform** any given set of **patterns** to a standard form using a simple procedure. Then we demonstrate that after a competitive initialization among all layers our multilayer network converges in one step to **fixed points** which are one of the given **patterns** in its standard form. Due to an increase in the domain of attraction, our architecture...

Wang, L. (editor) ; Rajapakse, J.C. (editor); Fukushima, K. (editor); Lee, S-Y. (editor); Yao, X...

?

File 347:JAPIO Nov 1976-2005/Aug(Updated 051205)
(c) 2005 JPO & JAPIO
File 350:Derwent WPIX 1963-2005/UD,UM &UP=200601
(c) 2006 Thomson Derwent

Set	Items	Description
S1	141173	(CONVERT????? OR CONVER??????? OR TRANSFORM????? OR CHANG?- ??? OR TRANSLAT????) (7N) (FORMAT?? OR DESIGN?? OR STRUCTU???? - OR ARRANGE????? OR PATTERN???)
S2	14622	FIX?? (2N) POINT??
S3	767	S2 (7N) (FORMAT?? OR DESIGN?? OR STRUCTU???? OR ARRANGE????? OR PATTERN???)
S4	1588331	IMAG?? OR GRAPHIC??? OR PICTURE??
S5	252192	(RENDER???? OR PROCESS????) (5N) S4
S6	4039	AU=(WANG L? OR WANG, L? OR DENG K? OR DENG, K? OR GUO B? OR GUO, B? OR BUCKMAN J? OR BUCKMAN, J?)
S7	16	S1 AND S3 AND S4
S8	1	S6 AND S1 AND S3 AND S4
S9	0	S8 NOT S7

7/3,K/1 (Item 1 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2005 JPO & JAPIO. All rts. reserv.

07474261 **Image available**

IMAGE GENERATING SYSTEM AND **IMAGE** GENERATING METHOD AND INFORMATION
STORAGE MEDIUM

PUB. NO.: 2002-342779 [JP 2002342779 A]
PUBLISHED: November 29, 2002 (20021129)
INVENTOR(s): YOKOTA TAKASHI
MURATA HIROYUKI
APPLICANT(s): NAMCO LTD
APPL. NO.: 2001-144676 [JP 2001144676]
FILED: May 15, 2001 (20010515)

IMAGE GENERATING SYSTEM AND **IMAGE** GENERATING METHOD AND INFORMATION
STORAGE MEDIUM

ABSTRACT

PROBLEM TO BE SOLVED: To provide an **image** generating system, an **image** generating method, and an information storage medium, capable of executing try linear interpolation processing using...
... stored in a prescribed storage area by using the texture information of those faces. This **image** generating system is provided with a data **format** **changing** part 212 for **converting** the data **format** of the parameter for face selection expressed in a floating decimal point **format** into the data **format** in a **fixed** decimal **point** **format** at the time of generating a **image** by performing try linear interpolation processing based on the parameter for face selection in the...

...part 215 for performing prescribed bit arithmetic operation by using the parameter for face selection **converted** into the **fixed** decimal **point** **format** .

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7/3,K/2 (Item 2 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2005 JPO & JAPIO. All rts. reserv.

06645640 **Image available**

GRAPHIC DATA TRANSFER DEVICE AND **GRAPHIC** OUTPUT SYSTEM

PUB. NO.: 2000-231456 [JP 2000231456 A]
PUBLISHED: August 22, 2000 (20000822)
INVENTOR(s): MIYAZONO KENZO
APPLICANT(s): FUJI XEROX CO LTD
APPL. NO.: 11-030766 [JP 9930766]
FILED: February 09, 1999 (19990209)

GRAPHIC DATA TRANSFER DEVICE AND **GRAPHIC** OUTPUT SYSTEM

ABSTRACT

...TO BE SOLVED: To reduce transferred data quantity when pieces of data of characters and **graphics** the shape of which is expressed by control points are transferred to an **image** output device such as a printer.

SOLUTION: In an OS(operating system) to supply font...

... a printer driver, control point coordinates of an outline font are managed by a source **format** 10 in a **fixed decimal point** form of an integer part with 16 bits and a decimal part with 16 bits. Each control point coordinate is **converted** into a transfer **format** 14 for small size when character size of a font is the size so as to be stored in the transfer **format** 14 for small size in the **fixed decimal point** form of the integer part with 10 bits and the decimal part with 6 bits...

... be stored in the transfer format 14 for small size, each control point coordinate is **converted** into a transfer **format** 12 for large size in a floating point form with 32 bits which are conventionally...

7/3,K/3 (Item 3 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2005 JPO & JAPIO. All rts. reserv.

05924588 **Image available**
GRAPHICS PROCESSOR

PUB. NO.: 10-207688 [JP 10207688 A]
PUBLISHED: August 07, 1998 (19980807)
INVENTOR(s): TAKAMI KAZUHISA
FUJITA MAKOTO
ABE YUICHI
SUZUKI KATSUNORI
ONIKI KAZUNORI
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP
(Japan)
APPL. NO.: 09-010918 [JP 9710918]
FILED: January 24, 1997 (19970124)

GRAPHICS PROCESSOR

ABSTRACT

PROBLEM TO BE SOLVED: To realize high speed **graphics** processing...

...factor data for specifying the decimal point bit position of fixed point data to be **converted** from each floating point data. A **format converter** 100 **converts** each floating **point** data into **fixed point** data having the decimal point bit position specified by the scale factor data and transfers...

7/3,K/4 (Item 4 from file: 347)
DIALOG(R)File 347:JAPIO
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01790044 **Image available**
DIFFRACTION PATTERN DISPLAY METHOD BY ELECTRON MICROSCOPE

PUB. NO.: 61-004144 [JP 61004144 A]
PUBLISHED: January 10, 1986 (19860110)
INVENTOR(s): KONDO KOJIN
APPLICANT(s): JEOL LTD [000427] (A Japanese Company or Corporation), JP
(Japan)
APPL. NO.: 59-123398 [JP 84123398]
FILED: June 15, 1984 (19840615)

JOURNAL: Section: E, Section No. 406, Vol. 10, No. 141, Pg. 51, May
24, 1986 (19860524)

ABSTRACT

PURPOSE: To allow a large-angle **convergent** electron diffraction **pattern** based on an extremely fine sample region to be displayed by adding an auxiliary lens magnetic field and **image** shifting deflection coils between an objective lens magnetic field and a visual field restricting iris...

... to a sample 4 is angle-scanned by deflection coils 2, 3 with its illumination **point fixed**, (B) a diffraction **pattern** generated on a focal plane F behind an objective lens 6 is **image**-formed by an auxiliary lens 20 on the plane where an iris plate 7 is located, (C) the **image** is expansively projected on a fluorescent plate 10, (D) a shift of the diffraction pattern due to the angle scanning is stopped by **image** shifting deflection coils 21, 22, (E) the iris plate 7 is arranged to selectively extract...

7/3,K/5 (Item 5 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2005 JPO & JAPIO. All rts. reserv.

01593971 **Image available**
PICTURE READER

PUB. NO.: 60-072471 [JP 60072471 A]
PUBLISHED: April 24, 1985 (19850424)
INVENTOR(s): MATSUSHITA HARUKAZU
APPLICANT(s): SEIKO EPSON CORP [000236] (A Japanese Company or Corporation)
, JP (Japan)
APPL. NO.: 58-181215 [JP 83181215]
FILED: September 29, 1983 (19830929)
JOURNAL: Section: E, Section No. 338, Vol. 09, No. 208, Pg. 149,
August 24, 1985 (19850824)

PICTURE READER

ABSTRACT

PURPOSE: To obtain a simple linear light source for **picture** reading with high output and uniform constitution by designing the linear light source where one...

... of the optical fiber 13 are collected cylindrically and fixed and the other ends are **arranged** linearly and **fixed** to **convert point** light sources into a linear light source. The light guide 14 made of a transparent...

...luminous amount due to pitch difference of the optical fiber with almost no loss. The **image** of the light from an original face is formed on a photoelectric converter 21 by an **image** forming lens 20. The figure shows an example where a CCD is used for the...

7/3,K/6 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

016936736 **Image available**
WPI Acc No: 2005-261046/200527

XRPX Acc No: N05-214323

Graphics rendering method for embedded device e.g. mobile computing device, involves processing rendering data in variable-length fixed - point format , and rendering processed rendering data on embedded device

Patent Assignee: MICROSOFT CORP (MICT)

Inventor: BUCKMAN J W; DENG K; GUO B; WANG L

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20050062762	A1	20050324	US 2003661055	A	20030913	200527 B

Priority Applications (No Type Date): US 2003661055 A 20030913

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20050062762	A1		28	G09G-005/00	

Graphics rendering method for embedded device e.g. mobile computing device, involves processing rendering data in variable-length fixed - point format , and rendering processed rendering data on embedded device

Abstract (Basic):

... The method involves inputting a rendering data in one **format** .

The rendering data from the **format** is **converted** into a variable length **fixed - point format** . The rendering data in the variable-length **fixed - point format** is processed. The processed rendering data is rendered on an embedded device. A mathematical library is created for processing the rendering data in the variable-length **fixed - point format** .

... computer-readable medium having computer-executable instructions for performing the computer-implemented method for rendering **graphics** (...)

...B) a **graphics** rendering system for an embedded computing device...

...Used for rendering **graphics** on an embedded device e.g. mobile computing device...

...The data is rendered in the variable-length **fixed - point format** , thus enabling efficient and fast **graphics** rendering in the embedded devices. The **graphics** rendering method are software implemented, thus avoiding requirement of powerful **graphics** and processing hardware

Title Terms: **GRAPHIC** ;

7/3,K/7 (Item 2 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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015200786 **Image available**

WPI Acc No: 2003-261320/200326

XRPX Acc No: N03-207214

Three-dimensional human face shape generation method using computer graphics , involves changing surface of basic 3D face shape by changing arrangement of shape control points provided at fixed intervals to 3D face shape

Patent Assignee: NIPPON TELEGRAPH & TELEPHONE CORP (NITE)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2003030684	A	20030131	JP 2001209630	A	20010710	200326 B

Priority Applications (No Type Date): JP 2001209630 A 20010710

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 2003030684	A		11	G06T-017/40	

Three-dimensional human face shape generation method using computer graphics , involves changing surface of basic 3D face shape by changing arrangement of shape control points provided at fixed intervals to 3D face shape

Abstract (Basic):

... of a basic three-dimensional face shape. The surface of the three-dimensional shape is **changed** by **changing** the **arrangement** of the shape control points.
... For producing three-dimensional shape of human face, using computer **graphics** .

...Title Terms: **GRAPHIC** ;

7/3,K/8 (Item 3 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

015011863 **Image available**

WPI Acc No: 2003-072380/200307

XRPX Acc No: N03-056557

Image forming system performs linear interpolation process using texture information on surface, based on predetermined bit calculation performed using fixed - point format of surface choice parameter

Patent Assignee: NAMCO LTD (NAMC-N)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2002342779	A	20021129	JP 2001144676	A	20010515	200307 B

Priority Applications (No Type Date): JP 2001144676 A 20010515

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 2002342779	A		20	G06T-015/00	

Image forming system performs linear interpolation process using texture information on surface, based on predetermined bit calculation performed using fixed - point format of surface choice parameter

Abstract (Basic):

... An alteration unit (212) **changes** the floating-point data **format** of surface choice parameter, into a **fixed point format** .
An arithmetic unit (215) performs a predetermined bit calculation using the output of the alteration...
... 1) **Image** formation method; and...

...2) Information storage medium storing **image** formation program...

... **Image** forming system...

...The figure shows the block diagram of the arithmetic section of the **image** forming system. (Drawing includes non-English language text

Title Terms: **IMAGE** ;

7/3,K/9 (Item 4 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

014582573 **Image available**
WPI Acc No: 2002-403277/200243
XRPX Acc No: N02-316391

Image encoder for mobile terminal e.g. PDA, has wavelet conversion section including fixed-point wavelet converter and integer wavelet converter to perform wavelet conversion filtering on image areas
Patent Assignee: SONY CORP (SONY); FUKUHARA T (FUKU-I); KIMURA S (KIMU-I)
Inventor: FUKUHARA T; KIMURA S
Number of Countries: 002 Number of Patents: 002
Patent Family:
Patent No Kind Date Applicat No Kind Date Week
US 20020028022 A1 20020307 US 2001919925 A 20010802 200243 B
JP 2002058026 A 20020222 JP 2000240464 A 20000808 200243

Priority Applications (No Type Date): JP 2000240464 A 20000808
Patent Details:
Patent No Kind Lan Pg Main IPC Filing Notes
US 20020028022 A1 26 G06K-009/36
JP 2002058026 A 17 H04N-007/30

Image encoder for mobile terminal e.g. PDA, has wavelet conversion section including fixed-point wavelet converter and integer wavelet converter to perform wavelet conversion filtering on image areas

Abstract (Basic):

... A memory (1) stores predetermined **image** areas of input **image** data (100). A wavelet conversion section (5) including a fixed-point wavelet converter (3) and an integer wavelet converter (4), performs wavelet conversion filtering on the **image** areas, in horizontal or vertical direction, as soon as the **image** data is stored in the memory.
... a) **Image** coding method...
...b) **Image** decoder...
...c) **Image** decoding method...
...For encoding still **images** or moving **pictures** for **image** transmission by mobile terminals such as PDA, portable telephone, etc
...
...Reduces increase of entire hardware components by adopting a **structure** common to the **fixed - point** and integer precision wavelet converters. Performs fixed-point type wavelet conversion with higher precision. The
...
...the compression rate, and without increasing power consumption. Increases degree of freedom in selecting the **image** quality and compression rate, without enlarging the hardware structure...
...The figure shows the block diagram of schematic **structure** of wavelet **converter** .
...

...Input **image** data (100

Technology Focus:

... The wavelet conversion for **image** compression is performed according to standards including JPEG-2000.

Title Terms: **IMAGE** ;

7/3,K/10 (Item 5 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014456216 **Image available**

WPI Acc No: 2002-276919/200232

XRPX Acc No: N02-216381

Digital signal processor for computer, has calculator which performs addition of data having specific format read from memories and stores addition result in different format into register

Patent Assignee: SONY CORP (SONY)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2002063152	A	20020228	JP 2000253018	A	20000817	200232 B

Priority Applications (No Type Date): JP 2000253018 A 20000817

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 2002063152	A		9	G06F-017/10	

Abstract (Basic):

... For computer for processing digitized information such as multimedia data, digitized still **picture image** , moving **image** , audio signals, etc...
...in circuit, hence achieves cost reduction. Achieves reduction in processing load, thereby increasing performance efficiency. **Format conversion of fixed point** data is performed easily and the load of software development is reduced. Memory addresses are...

7/3,K/11 (Item 6 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014029783 **Image available**

WPI Acc No: 2001-513997/200156

Related WPI Acc No: 2001-456683

XRPX Acc No: N01-380786

Floating point to integer format conversion method for multimedia application, involves converting floating point number stored in register to integer number which is stored in another register

Patent Assignee: ABDALLAH M A F (ABDA-I); HSIEH H E (HSIE-I); HUFF T R (HUFF-I); PENTKOVSKI V (PENT-I); ROUSSEL P (ROUS-I); THAKKAR S S (THAK-I); INTEL CORP (ITLC)

Inventor: ABDALLAH M A F; HSIEH H E; HUFF T R; PENTKOVSKI V; ROUSSEL P; THAKKAR S S

Number of Countries: 001 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20010016902	A1	20010823	US 9870900	A	19980430	200156 B
			US 2001844728	A	20010427	

graphic data group of 3D object to floating point format, to perform pipeline processing

...Abstract (Basic): NOVELTY - The Z co-ordinate of **graphic data group** of a 3D object expressed in **fixed point format** is **converted** into floating point **format** , by adding a common Z exponent value so that pipeline processing of 3D object is performed. DETAILED DESCRIPTION - The floating point **format** includes mantissa and exponent portions while **converting** floating point **format** of Z co-ordinate of the specific portion of the 3D object to **fixed point format** . The exponent portion of the float is removed based on the predetermined exponent value. An...

...USE - For used during pipeline processing in 3D **graphics** .

...

...ADVANTAGE - Since **conversion** of **fixed** to floating point **format** and vice-versa depends on the mantissa and exponent portions of a float, high numerical

...Title Terms: **GRAPHIC** ;

7/3,K/13 (Item 8 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011638481 **Image available**

WPI Acc No: 1998-055389/199806

Related WPI Acc No: 2000-080035

XRPX Acc No: N98-043893

Determining Z-values of hidden surfaces of three-dimensional graphics - representing surface of each object with primitives with floating point vertices and comparing to determine largest exponent

Patent Assignee: SUN MICROSYSTEMS INC (SUNM)

Inventor: DEERING M F

Number of Countries: 020 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 817126	A2	19980107	EP 97110786	A	19970701	199806 B
JP 10116357	A	19980506	JP 97188922	A	19970701	199828
US 6046746	A	20000404	US 96673117	A	19960701	200024
EP 817126	B1	20021218	EP 97110786	A	19970701	200301
DE 69717919	E	20030130	DE 617919	A	19970701	200317
			EP 97110786	A	19970701	

Priority Applications (No Type Date): US 96673117 A 19960701

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 817126	A2	E	13	G06T-015/10	
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Designated States (Regional): AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

JP 10116357	A		14	G06T-015/40	
-------------	---	--	----	-------------	--

US 6046746	A			G06F-017/10	
------------	---	--	--	-------------	--

EP 817126	B1	E		G06T-015/10	
-----------	----	---	--	-------------	--

Designated States (Regional): DE FR GB IT NL SE

DE 69717919	E			G06T-015/10	Based on patent EP 817126
-------------	---	--	--	-------------	---------------------------

Determining Z-values of hidden surfaces of three-dimensional graphics -

...Abstract (Basic): the values of the vertices associated with the

primitive. The result of the subtracting is **converted** to **fixed point format** . Set-up and/or scan **conversion** video display processes are then performed. The final Z-values output from a scan conversion...
...Title Terms: **GRAPHIC** ;

7/3,K/14 (Item 9 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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010290092 **Image available**
WPI Acc No: 1995-191351/199525
XRPX Acc No: N95-150098

Image pattern automatic inspection method used for film carriers - examines master pattern for error and quality through initialisation of measured patterns

Patent Assignee: NIPPON AVIONICS CO LTD (NIAV-N)
Number of Countries: 001 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 7110863	A	19950425	JP 93279012	A	19931012	199525 B
JP 3316977	B2	20020819	JP 93279012	A	19931012	200261

Priority Applications (No Type Date): JP 93279012 A 19931012
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 7110863	A		9	G06T-007/00	
JP 3316977	B2		9	G06T-007/00	Previous Publ. patent JP 7110863

Image pattern automatic inspection method used for film carriers...

...Abstract (Basic): The method begins by storing the best part of the measured pattern in the **image** memory unit (12) as a shaded **image** of the master pattern. Digitisation is performed on the shade histogram from the memory. The master **pattern** (M) is **converted** to a linear assembly with minimum power consumption. In looking for the main line (LM...

...is compared to the measured pattern from the memory. The inspection range of the measured **pattern** is set and inspected in a **fixed point** within the range. The length (delta) of the perpendicular line w.r.t. the straight...

Title Terms: **IMAGE** ;

7/3,K/15 (Item 10 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2006 Thomson Derwent. All rts. reserv.

010249075 **Image available**
WPI Acc No: 1995-150330/199520
Related WPI Acc No: 1995-071742; 1995-071745; 1995-103220
XRAM Acc No: C95-069566
XRPX Acc No: N95-118038

Exposure method for transferring circuit pattern onto sensitised substrate - using judgement unit to determine if computed gap is within preset limit and if it is not so, reticular alignment operation is performed

Patent Assignee: NIKON CORP (NIKR)

Inventor: IMAI Y; MIYAI T; SUZUKI K; TANIGUCHI T

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 7074075	A	19950317	JP 93217675	A	19930901	199520 B
US 5581324	A	19961203	US 94254780	A	19940606	199703
			US 95446511	A	19950522	

Priority Applications (No Type Date): JP 93217675 A 19930901; JP 93138488 A 19930610; JP 93166504 A 19930611; JP 93174162 A 19930714

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 7074075	A		9		
US 5581324	A		35		Cont of application US 94254780 patent JP 6349700 patent JP 6349703 patent JP 7029802

...Abstract (Basic): results in the transfer of a pattern from the mask onto a sensitized substrate. The **pattern** domain undergoes heat **transformation** due to light beam irradiation. This is measured by the positional variation of a main **point** (RC) fixed in the **pattern** domain. Using this heat **transformation** value, the same is calculated for two or more points (α 1- α 4) in...

...Then, the maximum gap 'Ds' that arises due to shifting of the **pattern** domain after heat **transformation** along X-Y directions is computed. A judgment unit is used to determined whether the...

...DVANTAGE - For use in semiconductor IC fabrication. Does not reduce through put. Performs nearly precise **image** formation. Does not degrade quality of **image** formed...

...Abstract (Equivalent): pattern is formed, with the illumination light, and a projection optical system for forming an **image** of the pattern on a photosensitive substrate, and **images** the **image** of the pattern on the photosensitive substrate in a predetermined imaging state, comprising: a temp...

7/3,K/16 (Item 11 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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007255132

WPI Acc No: 1987-252139/198736

XRPX Acc No: N87-188693

Geometry processor for graphics display system - is single-board, pipelined, programmable fixed - point or designed to execute geometric algorithms

Patent Assignee: GENERAL ELECTRIC CO (GENE); STAR TECH INC (STAR-N)

Inventor: STEINER W R

Number of Countries: 004 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2187615	A	19870909	GB 874601	A	19870227	198736 B
DE 3706509	A	19870910	DE 3706509	A	19870228	198737
FR 2595486	A	19870911				198744
US 4862392	A	19890829	US 86838300	A	19860307	198944
GB 2187615	B	19900404				199014

Priority Applications (No Type Date): US 86838300 A 19860307

Patent Details:

Patent No	Kind	Lang	Pg	Main IPC	Filing Notes
US 4862392	A		23		

Geometry processor for graphics display system...

...is single-board, pipelined, programmable fixed - point or designed to execute geometric algorithms

...Abstract (Basic): The processor comprises a culling device coupled to a memory storing segments of **graphics** data which represent three-dimensional objects. The device compares in a first processing phase at...

...each segment from the memory to preset values for selecting whether each such segment contains **graphics** data this is at least partly within a defined viewing volume...

...the selected segments back from the second memory in a second processing phase, transforms the **graphics** data in the selected segments from a three-dimensional to a two-dimensional representation, and transfers the transformed **graphics** data to a display system...

...Abstract (Equivalent): An electronic geometry processor circuit adapted for use in a **graphics** display system comprising

...Abstract (Equivalent): The geometry process or for use in a **graphics** processing system is adapted to couple with a hierarchically structured **graphics** database memory, a special purpose processor for traversing the database, and a display processor. The...

...data bus to the special purpose traversing processor, a second private data bus to the **graphics** database memory, a high-speed arithmetic processing module, a double-buffered output register, and a...

...The geometry processor is configured to process the **graphics** database in two passes. The first pass is a culling operation that culls out **graphics** data supplied from the database memory that is outside of a defined viewing volume, with...

...additional associated data from the database memory, from the traversing processor's stack memory and **transforms** that data from a three-dimensional mathematical **format** to a two-dimensional format suitable for display on a video display system. (23pp).

...Title Terms: **GRAPHIC** ;

?

File 348:EUROPEAN PATENTS 1978-2005/Dec W04

(c) 2005 European Patent Office

File 349:PCT FULLTEXT 1979-2005/UB=20051229,UT=20051222

(c) 2005 WIPO/Univentio

Set	Items	Description
S1	206670	(CONVERT????? OR CONVER??????? OR TRANSFORM????? OR CHANG?- ??? OR TRANSLAT????) (7N) (FORMAT?? OR DESIGN?? OR STRUCTU???? - OR ARRANGE????? OR PATTERN???)
S2	19965	FIX?? (2N) POINT??
S3	1319	S2 (7N) (FORMAT?? OR DESIGN?? OR STRUCTU???? OR ARRANGE????? OR PATTERN???)
S4	584713	IMAG?? OR GRAPHIC??? OR PICTURE??
S5	105279	(RENDER???? OR PROCESS????) (5N) S4
S6	1002	AU= (WANG L? OR WANG, L? OR DENG K? OR DENG, K? OR GUO B? OR GUO, B? OR BUCKMAN J? OR BUCKMAN, J?)
S7	17	S1 (S) S3 (S) S4
S8	5	S1 (35N) S3 (35N) S5
S9	1	S8 NOT S7

7/3,K/1 (Item 1 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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01554983

Time code calculating method and time code calculating apparatus

Verfahren und Vorrichtung zur Zeitkodeberechnung

Procede et appareil pour calculer un code temporel

PATENT ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., (216883), 1006, Oaza-Kadoma,
Kadoma-shi, Osaka 571-8501, (JP), (Applicant designated States: all)

INVENTOR:

Hosoda, Takaharu, 5-20-506, Tachibana 2-chome, Nishinari-ku, Osaka-shi,
Osaka 557-0051, (JP)

LEGAL REPRESENTATIVE:

TER MEER STEINMEISTER & PARTNER GbR (100061), Patentanwalte,
Mauerkircherstrasse 45, 81679 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 1293983 A2 030319 (Basic)

EP 1293983 A3 030625

APPLICATION (CC, No, Date): EP 2002020398 020912;

PRIORITY (CC, No, Date): JP 2001278209 010913

DESIGNATED STATES: DE; FR; GB

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G11B-027/32; G11B-027/30

ABSTRACT WORD COUNT: 97

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200312	1720
SPEC A	(English)	200312	25454
Total word count - document A			27174
Total word count - document B			0
Total word count - documents A + B			27174

...SPECIFICATION Embodiment

A fourth embodiment provides a time code calculating apparatus to be used when converting **image** data of a 30 frame/second type into **image** data of a 24 frame/second type (a non-drop mode), in which the present...

...code calculating apparatus, a time code is converted in such a state that a synchronous **point** is **fixed**.

First of all, the schematic **structure** of the time code calculating apparatus according to the present embodiment will be described with...

7/3,K/2 (Item 2 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

01438940

Method of obtaining an image

Verfahren zur Herstellung eines Bildes

Procede d'obtention d'une image

PATENT ASSIGNEE:

Synoptics Limited, (2628860), Beacon House, Nuffield Road, Cambridge CB4
1TF, (GB), (Applicant designated States: all)

INVENTOR:

Atkin, Philip, 93 A New Road, Haslingfield, Cambridge CB3 7LP, (GB)
LEGAL REPRESENTATIVE:
Brunner, Michael John (28871), GILL JENNINGS & EVERY, Broadgate House, 7
Eldon Street, London EC2M 7LH, (GB)
PATENT (CC, No, Kind, Date): EP 1225756 A1 020724 (Basic)
APPLICATION (CC, No, Date): EP 2001300025 010103;
DESIGNATED STATES: DE; GB
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS: H04N-001/407; H04N-005/235
ABSTRACT WORD COUNT: 147
NOTE:

Figure number on first page: 1
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200230	195
SPEC A	(English)	200230	2224
Total word count - document A			2419
Total word count - document B			0
Total word count - documents A + B			2419

...SPECIFICATION vmin)). Finally, the ratio of the two accumulation values
is calculated to form a result **image**. This **image** may be subject to a
linear rescaling or offset to make it suitable for **conversion** to a
fixed point format image for further processing.

7/3,K/3 (Item 3 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01273192

Vertex cache for 3D computer graphics

Vertexcache fur 3D Rechnergrafik

Cache pour vertex pour graphique 3D par ordinateur

PATENT ASSIGNEE:

Nintendo Co., Limited, (769164), 60 Fukuine, Kamitakamatsu-cho,
Higashiyama-ku, Kyoto-shi, Kyoto 605-8660, (JP), (Applicant designated
States: all)

ATI Technologies Inc., (3024390), 33 Commerce Valley Drive East,
Thornhill, Ontario L3T 7N6, (CA), (Applicant designated States: all)

INVENTOR:

Fouladi, Farhad, 101 Alma Street, Palo Alto, CA 94301, (US)

Van Hook, Timothy J., 224 Oak Grove Avenue, Atherton, CA 94027, (US)

Cheng, Howard H., 2005 212th Place NE, Redmond, Washington 98053, (US)

Moore, Robert, 2522 227th Place NE, Redmond, Washington 98053, (US)

LEGAL REPRESENTATIVE:

Horton, Andrew Robert Grant et al (32021), BOWLES HORTON Felden House
Dower Mews High Street, Berkhamsted Hertfordshire HP4 2BL, (GB)

PATENT (CC, No, Kind, Date): EP 1096427 A2 010502 (Basic)
EP 1096427 A3 030416

APPLICATION (CC, No, Date): EP 2000309315 001023;

PRIORITY (CC, No, Date): US 161915 991028; US 465754 991217

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06T-015/00

ABSTRACT WORD COUNT: 170

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200118	545
SPEC A	(English)	200118	4944
Total word count - document A			5489
Total word count - document B			0
Total word count - documents A + B			5489

...SPECIFICATION this invention, the vertex data includes quantized, compressed data streams in any of several different **formats** (e.g., 8-bit **fixed point**, 16-bit **fixed point**, or floating point). This data can be indexed (i.e., referenced by the vertex data...
...data formats can all be stored in the common vertex cache, and subsequently decompressed and **converted** into a common **format** for the **graphics** display pipeline. Such hardware support of flexible types, formats and numbers of attributes as either...

7/3,K/4 (Item 4 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00782478

Color format conversion in a parallel processor

Farbformatumwandlung in einem Parallelprozessor

Conversion de format de couleur dans un processeur parallele

PATENT ASSIGNEE:

SUN MICROSYSTEMS, INC., (1392730), 2550 Garcia Avenue, Mountain View, CA 94043, (US), (Proprietor designated states: all)

INVENTOR:

Zhou, Chang-Guo, 20333 Bollinger Road, Cupertino, California 95014, (US)

Rice, Daniel S., 5838 Birch Court, No. F, Oakland, California 94618, (US)

LEGAL REPRESENTATIVE:

Johnson, Terence Leslie (42961), Marks & Clerk Incorporating Edward Evans

Barker Clifford's Inn, Fetter Lane, London EC4A 1BZ, (GB)

PATENT (CC, No, Kind, Date): EP 730386 A2 960904 (Basic)

EP 730386 A3 980826

EP 730386 B1 040915

EP 730386 B1 040915

APPLICATION (CC, No, Date): EP 96301431 960301;

PRIORITY (CC, No, Date): US 398111 950303

DESIGNATED STATES: DE; FR; GB; NL; SE

INTERNATIONAL PATENT CLASS: H04N-009/64

ABSTRACT WORD COUNT: 234

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	2860
CLAIMS B	(English)	200438	2030
CLAIMS B	(German)	200438	1753
CLAIMS B	(French)	200438	2629
SPEC A	(English)	EPAB96	5731
SPEC B	(English)	200438	5697
Total word count - document A			8593
Total word count - document B			12109
Total word count - documents A + B			20702

...SPECIFICATION Generally, component color information of a color pixel is stored in 8-bit, unsigned integer **format** . **Conversion** of three 16-bit **fixed point** color components of a pixel to three 8-bit unsigned integer color components of a...

...than 255. This latter comparison and replacement scheme is generally referred to as "clipping." The **graphics** processor described briefly above and more completely below scales and clips four (4) 16-bit...

...SPECIFICATION Generally, component color information of a color pixel is stored in 8-bit, unsigned integer **format** . **Conversion** of three 16-bit **fixed point** color components of a pixel to three 8-bit unsigned integer color components of a...

...than 255. This latter comparison and replacement scheme is generally referred to as "clipping." The **graphics** processor described briefly above and more completely below scales and clips four (4) 16-bit...

7/3,K/5 (Item 5 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00711605

Reconfigurable data processing stage

Rekonfigurierbare Datenverarbeitungsstufe

Etage d'operation de donnees reconfigurable

PATENT ASSIGNEE:

DISCOVISION ASSOCIATES, (260273), 2355 Main Street Suite 200, Irvine, CA 92714, (US), (Proprietor designated states: all)

INVENTOR:

Wise, Adrian Philip, 10 Westbourne Cottages, Frenchay, Bristol, BS16 1NA, (GB)

Sotheran, Martin William, The Ridings, Wick Lane, Stinchcombe, Dursley, Gloucestershire, GL11 6BD, (GB)

Robbins, William Philip, 19 Springhill, Cam, Gloucestershire, GL11 5PE, (GB)

LEGAL REPRESENTATIVE:

Vuillermoz, Bruno et al (72791), Cabinet Laurent & Charras B.P. 32 20, rue Louis Chirpaz, 69131 Ecully Cedex, (FR)

PATENT (CC, No, Kind, Date): EP 674446 A2 950927 (Basic)

EP 674446 A3 960814

EP 674446 B1 010801

APPLICATION (CC, No, Date): EP 95301300 950228;

PRIORITY (CC, No, Date): GB 9405914 940324

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IE; IT; LI; NL

INTERNATIONAL PATENT CLASS: H04N-007/24; G06F-013/00; G06F-009/38

ABSTRACT WORD COUNT: 144

NOTE:

Figure number on first page: 10

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB95	2475
CLAIMS B	(English)	200131	1079
CLAIMS B	(German)	200131	1072
CLAIMS B	(French)	200131	1186
SPEC A	(English)	EPAB95	125236
SPEC B	(English)	200131	121335
Total word count - document A			127738

Total word count - document B 124672
Total word count - documents A + B 252410

...SPECIFICATION formats
(MPEG video resolutions up to 704x480, 30 Hz, 4:2:0
(Flexible chroma sampling **formats**
(Can re-order the MPEG **picture** sequence
(Glue-less DRAM interface
(Single +5V supply
(208 pin PQFP package
(Max. power dissipation...

7/3,K/6 (Item 6 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00601469

Processing image data.

Bilddatenverarbeitung.

Traitement de donnees d'image.

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku,
Tokyo, (JP), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

Billyard, A.M., c/o Canon Res. Centre Europe Ltd., Units 17/20, Frederick
Sanger Rd, Surrey Res. Park, Guildford, Surrey GU2 5YD, (GB)

Huddy, R.S., c/o Canon Res. Centre Europe Ltd., Units 17/20, Frederick
Sanger Rd, Surrey Res. Park, Guildford, Surrey GU2 5YD, (GB)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick
Court High Holborn, London WC1R 5DJ, (GB)

PATENT (CC, No, Kind, Date): EP 590979 A2 940406 (Basic)
EP 590979 A3 940615

APPLICATION (CC, No, Date): EP 93307772 930930;

PRIORITY (CC, No, Date): GB 9220766 921002

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06F-015/72;

ABSTRACT WORD COUNT: 91

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF2	1087
SPEC A	(English)	EPABF2	11975
Total word count - document A			13062
Total word count - document B			0
Total word count - documents A + B			13062

...SPECIFICATION products of light source factors and object normal vectors
are calculated using fixed point arithmetic. **Conversion** from floating
point to fixed point format may for example be performed on an
object normal vector, prior to the calculation of...

...from a consideration of the embodiments described below. The invention
further provides methods for processing **image** data in accordance with
the invented principles set forth above, and provides **image** signals and
recordings in whatever form that have been generated in accordance with
those principles...

7/3,K/7 (Item 7 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00405202

Method of convoluting digital images
Verfahren zur Faltung von numerischen Bildern
Methode pour faire la convolution des images numeriques

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

Gonzalez-Lopez, Jorge, 8 Hewlett Road, Red Hook, New York 12571, (US)
Liang, Bob Chao-Chu, Box 522, Ryan Drive, West Hurley, New York 12401,
(US)

Tannenbaum, David Conrad, Fairview Gardens, B6A3, Kingston, New York
12401, (US)

LEGAL REPRESENTATIVE:

Burt, Roger James, Dr. et al (52152), IBM United Kingdom Limited
Intellectual Property Department Hursley Park, Winchester Hampshire
SO21 2JN, (GB)

PATENT (CC, No, Kind, Date): EP 396517 A1 901107 (Basic)
EP 396517 B1 960117

APPLICATION (CC, No, Date): EP 90850123 900402;

PRIORITY (CC, No, Date): US 344721 890428

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06T-005/20;

ABSTRACT WORD COUNT: 125

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	615
CLAIMS B	(English)	EPAB96	246
CLAIMS B	(German)	EPAB96	251
CLAIMS B	(French)	EPAB96	284
SPEC A	(English)	EPABF1	3947
SPEC B	(English)	EPAB96	3238
Total word count - document A			4562
Total word count - document B			4019
Total word count - documents A + B			8581

...SPECIFICATION or equal to the maximum number allowed, in the preferred
embodiment, 255. Finally, processor 166 **converts** the data from
floating **point format** to **fixed point format** and sends it to the
video pixel memory.

The zoom factor, T, indicates the number...

7/3,K/8 (Item 8 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00339460

Multi-screen setting condition display system
Anzeigesystem der Betriebszustände mit einem Bildschirm mit mehreren
Feldern

Systeme d'affichage de parametres selectionnees utilisant un ecran subdivise
en zones multiples

PATENT ASSIGNEE:

FUJI XEROX CO., LTD., (450440), No. 3-5, Akasaka 3-chome, Minato-ku Tokyo
107, (JP), (applicant designated states: DE;GB)

INVENTOR:

Shibayama, Yoshinaru, c/o Fuji Xerox Co., Ltd. Ebina Works, 2274, Hongo
Ebina-shi Kanagawa, (JP)

Ohtake, Takao, c/o Fuji Xerox Co., Ltd. Ebina Works, 2274, Hongo
Ebina-shi Kanagawa, (JP)

LEGAL REPRESENTATIVE:

Grunecker, Kinkeldey, Stockmair & Schwanhauser Anwaltssozietat (100721)
, Maximilianstrasse 58, 80538 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 334327 A2 890927 (Basic)

EP 334327 A3 901107

EP 334327 B1 931229

APPLICATION (CC, No, Date): EP 89105150 890322;

PRIORITY (CC, No, Date): JP 6838088 880323

DESIGNATED STATES: DE; GB

INTERNATIONAL PATENT CLASS: G06F-003/037

ABSTRACT WORD COUNT: 125

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9926	290
CLAIMS B	(German)	9926	261
CLAIMS B	(French)	9926	357
SPEC B	(English)	9926	25993
Total word count - document A			0
Total word count - document B			26901
Total word count - documents A + B			26901

...SPECIFICATION as shown in Fig. 9(a), is slidably mounted to a support
shaft 136 fixed to a lens carriage 135 arranged under the platen
glass 2. The lens 108 is connected to a lens motor Z...

...to a lens motor X 140 with wire (not shown), so that the magnification
is changed by moving the lens carriage 135 in the X direction
(horizontally in the drawing) along the support shaft 139...

...series A misses the carriage registration-sensor 155, the carriage
registration-sensor 155 generates a signal. The signal is sent to the
optical CPU 45 and is used for determining a...

7/3,K/9 (Item 9 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

00312375

Image processing method and apparatus.

Verfahren und Einrichtung zur Bildverarbeitung.

Procede et appareil de traitement d'image.

PATENT ASSIGNEE:

PIONEER ELECTRONIC CORPORATION, (537920), No. 4-1, Meguro 1-chome,
Meguro-ku Tokyo 153, (JP), (applicant designated states: DE;FR;GB)

INVENTOR:

Kanda, Masao c/o Pioneer Tokorozawa Plant, 2610, Hanazono 4-chome,
Tokorozawa-City Saitama, (JP)

LEGAL REPRESENTATIVE:

Sturt, Clifford Mark (50501), MARKS & CLERK 57-60 Lincoln's Inn Fields,
London WC2A 3LS, (GB)

PATENT (CC, No, Kind, Date): EP 291347 A2 881117 (Basic)
EP 291347 A3 890607
EP 291347 B1 940105
APPLICATION (CC, No, Date): EP 88304384 880513;
PRIORITY (CC, No, Date): JP 87118236 870515; JP 87140694 870604; JP
87141013 870605
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS: H04N-005/262; H04N-009/74; G06F-015/66;
ABSTRACT WORD COUNT: 199

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	624
CLAIMS B	(German)	EPBBF1	548
CLAIMS B	(French)	EPBBF1	658
SPEC B	(English)	EPBBF1	5730
Total word count - document A			0
Total word count - document B			7560
Total word count - documents A + B			7560

...SPECIFICATION and 13, the writing into the memory location of the memory 25 corresponding to each **point** of the regions A and B is **performed** at fixed timings. However, it is possible to **arrange** such that the timing of the writing is **changed** at predetermined intervals. For example, it may be possible to arrange such that the writing in...

...picture, so that a variety of movements can be created in the whole area of **picture** .

Fig. 15 is a block diagram **showing** an arrangement in which a further embodiment of the image processing method according the present...

7/3,K/10 (Item 10 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00306058

Digital data processing system.

Digitales Datenverarbeitungssystem.

Systeme de traitement de donnees numeriques.

PATENT ASSIGNEE:

DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581
, (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

INVENTOR:

Bachman, Brett L., 214 W. Canton Street Suite 4, Boston Massachusetts
02116, (US)

Bernstein, David H., 41 Bay Colony Drive, Ashland Massachusetts 01721,
(US)

Bratt, Richard Glenn, 9 Brook Trail Road, Wayland Massachusetts 01778,
(US)

Clancy, Gerald F., 13069 Jaccaranda Center, Saratoga California 95070,
(US)

Gavrin, Edward S., Beaver Pond Road RFD 4, Lincoln Massachusetts 01773,
(US)

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 PATENT (CC, No, Kind, Date): EP 290111 A2 881109 (Basic)
 EP 290111 A3 890503
 EP 290111 B1 931222
 APPLICATION (CC, No, Date): EP 88200917 820521;
 PRIORITY (CC, No, Date): US 266404 810522
 DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE
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 ABSTRACT WORD COUNT: 123

LANGUAGE (Publication,Procedural,Application): English; English; English
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Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1044
CLAIMS B	(German)	EPBBF1	890
CLAIMS B	(French)	EPBBF1	1185
SPEC B	(English)	EPBBF1	154314
Total word count - document A			0
Total word count - document B			157433
Total word count - documents A + B			157433

...SPECIFICATION of PC 10234 and comparison of access rights to intended
 operation is performed concurrently with **translation** of the memory
 request logical descriptor to a corresponding physical descriptor by ATU
 10228. If...

7/3,K/11 (Item 1 from file: 349)
 DIALOG(R)File 349:PCT FULLTEXT
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01129704

DEAD NOZZLE COMPENSATION

COMPENSATION D'UNE BUSE HORS ETAT DE FONCTIONNEMENT

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Patent and Priority Information (Country, Number, Date):
Patent: WO 200450369 A1 20040617 (WO 0450369)
Application: WO 2003AU1616 20031202 (PCT/WO AU03001616)
Priority Application: AU 2002953134 20021202; AU 2002953135 20021202
Designated States:
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prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NI NO NZ OM PG PH PL PT RO RU
SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) BW GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
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Fulltext Availability:
Claims

Claim

... the case of a SoPEC ISIMaster) ISI ping packets will be transmitted
according to the **pattern** given by the three PingScheduleN registers.
The ISI will start with the Isb of PingScheduleO...

7/3,K/12 (Item 2 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01118456 **Image available**

BUSINESS ANALYSIS AND MANAGEMENT SYSTEMS **SYSTEME D'ANALYSE ET DE GESTION DES ACTIVITES COMMERCIALES**

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200440402 A2-A3 20040513 (WO 0440402)
Application: WO 2003US31820 20031008 (PCT/WO US03031820)

Priority Application: US 2002417018 20021008; US 2002417098 20021008
Designated States:
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AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK
LR LS LT LU LV MA MD MG MK MN MW MX MZ NI NO NZ OM PG PH PL PT RO RU SC
SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 172008

Fulltext Availability:
Detailed Description

Detailed Description

... of
limitation, embodiments of the invention provide a basic system and
method for visualizing interaction **patterns** , called the issue view.
Rather than a **fixed** , formal hierarchy, this view displays interactions
between members of an organization against the background of...r

17

)in a

(DIE) in array

ple

Get

rarray

ent Class name. DCA

Purpose. **Transform** an array of data-collection

records (DIE) into an array of horizontal node

points (HNPJ...limited: for example, they typically produce only tabular
reports and cannot display data in a **graphic** , hierarchical format.

Representative applications in this category are report writers such as
Crystal Reports (Crystal...

...APPLICATIONS

The more useful way of processing the output data is to display them in
graphical form, superimposing the connections against a hierarchical
tree structure of persons or issues.

This approach...

...of an application that can read from the database and generate
instructions for creating a **graphical** tree structure. These
instructions are then passed to a display-engine application programming
interface (API...

...Multiple platforms.

Quesa (Open Source)

3,2.3 Graph libraries

A further enhancement of the **graphic** output display makes use of a
graphing API.

Representative examples include.

Boost Graph Library (Lee...

...analyzes these relationships with respect to specific organizational processes and issues presents the results in a **graphical** format
I .1 ENCOMPASS PROCESS OVERVIEW
The following broad outline characterizes the main steps in...people and issues. For each analysis, the software converts the data to a three-dimensional **graphical** display (fig. 5). By interpreting the display and creating additional analyses to examine specific types...
...the display of interaction links between members of an organization, What is missing from the **picture** is the structural context which gives meaning to these patterns of interaction and communication. This...
...lack of communication about its processess.

The EnCompass display engine converts these links to a **graphical** form for display and interpretation (see figure 5. page 4). Each link appears on a...

...database (diagram 1 1 t page 36). The display engine which converts these connections to **graphic** form for presentation is an existing module outside the scope of this discussion (see 3...limited: for example, they typically produce only tabular reports and cannot display data in a **graphic** , hierarc@ical format.

Representative applications in this category are report writers such as Crystal Reports...

...APPLICATIONS

The more useful way of processing the output data is to display them in **graphical** form, superimposing the connections against a hierarchical tree structure of persons or issues.

This approach...

...of an application that can read from the database and generate instructions for creating a **graphical** tree structure. These instructions are then passed to a display-engine application programming interface (AP...

7/3,K/13 (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01068569 **Image available**
POWER BASED LEVEL-OF-DETAIL MANAGEMENT SYSTEM FOR A PORTABLE COMPUTER GRAPHICS DISPLAY
SYSTEME DE GESTION DE NIVEAU DE DETAIL EN FONCTION DE LA PUISSANCE DESTINE A UN AFFICHAGE GRAPHIQUE D'ORDINATEUR PORTABLE
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Patent and Priority Information (Country, Number, Date):
Patent: WO 200398416 A2-A3 20031127 (WO 0398416)
Application: WO 2003US14595 20030509 (PCT/WO US03014595)
Priority Application: US 2002145234 20020513
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AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NI NO NZ OM PH PL PT RO RU SC SD SE
SG SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
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Fulltext Word Count: 7300

Fulltext Availability:
Detailed Description

Detailed Description
... the application level.

[17] FIG. 2 is a simplified block diagram of the sort middle **graphics**
architecture utilized in a preferred embodiment of the present invention.
As shown, the sort middle **graphics** architecture includes a host
interface 212, a series of geometry processing modules 214a-b, and...

...lines (BOBs) 222a-b. The geometry processing modules 214a-b each include
a float to **fixed** point data **format** module 220a-b for **converting** the
output from the geometry processing module from floating point **format**
into a **fixed point** data **format**. The host interface 212, also known
as a Heathrow system, is coupled to the processor that is running the
graphics application.

[18] FIG. 3 illustrates a flow chart that shows the non-nal operation of
...

7/3,K/14 (Item 4 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00910207 **Image available**
CONTINUOUS PRODUCTION AND PACKAGING OF PERISHABLE GOODS IN LOW OXYGEN
ENVIRONMENTS

PROCEDE DE PRODUCTION ET D'EMBALLAGE DE PRODUITS PERISSABLES DANS UNE
ATMOSPHERE PAUVRE EN OXYGENE

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200244026 A1 20020606 (WO 0244026)

Application: WO 2001US45146 20011128 (PCT/WO US0145146)

Priority Application: US 2000724287 20001128; US 2000255684 20001213; US 2001286688 20010426; US 2001291872 20010517; US 2001299240 20010618; US 2001312176 20010813; US 2001314109 20010821; US 2001323629 20010919; US 2001335760 20011019

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AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZM ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 197091

Fulltext Availability:

Claims

Claim

... wall 817, followed by an outwardly protruding first peak or ridge 825 that at one **point** can make contact with the outer cover 816. Continuing downward from the first peak, the...and fastened in any manner readily available to the base 4950. The conduit 4958 is **arranged** so as to be substantially gas tight and sealed in such a manner that any...

7/3,K/15 (Item 5 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00909145 **Image available**

PLANAR LASER ILLUMINATION AND IMAGING (PLIIM) SYSTEMS WITH INTEGRATED DESPECKLING MECHANISMS PROVIDED THEREIN
SYSTEMES PLIIM D'ILLUMINATION ET D'IMAGERIE AU LASER PLANAIRE A MECANISME DE DECHATOIEMENT INTEGRE

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Patent and Priority Information (Country, Number, Date):

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Application: WO 2001US44011 20011121 (PCT/WO US0144011)

Priority Application: US 2000721885 20001124; US 2001780027 20010209; US
2001781665 20010212; US 2001883130 20010615; US 2001954477 20010917; US
2001999687 20011031

Parent Application/Grant:

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AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL
TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

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Fulltext Word Count: 298301

Fulltext Availability:

Claims

Claim

... focal length of the image formation and detection module 3 is fixed during the optical **design** stage so that the **fixed** field of view thereof substantially matches the plane slightly above the conveyor belt 34 where...micro-oscillating the PLIB 393 prior to illuminating the target object. The lens array ring **structure** 392 can be made from a strip of holographic recording material 392A which has cylindrical... power reduction, it is expected that the lower threshold for this sample number at the **image** detection array can be expressed mathematically in terms of (i) the spatial gradient of the...

7/3,K/16 (Item 6 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00234265 **Image available**

SYSTEM FOR DIVIDING PROCESSING TASKS INTO SIGNAL PROCESSOR AND
DECISION-MAKING MICROPROCESSOR INTERFACING

SYSTEME DE SEPARATION DES TACHES DE TRAITEMENT EN TACHES POUR INTERFACAGE
AVEC UN PROCESSEUR DE SIGNAUX ET UN MICROPROCESSEUR DE PRISE DE
DECISION

Patent Applicant/Assignee:

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Patent and Priority Information (Country, Number, Date):

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AU CA JP KR AT BE CH DE DK ES FR GB GR IE IT LU MC NL SE

Publication Language: English

Fulltext Word Count: 219172

Fulltext Availability:

Claims

Claim

... 400a is able to read the boot code in boot ROM 190. The code is **arranged** to cause the GSP to seize control of the host port 800 and to load...to the data input port 700a in many ways, except that its function is the **converse**. The serial output port 700b includes a buffer 740, an parallel load shift register 750...DFM sections divide by N block. The data is then forwarded to the D/A **converter** 1070 where it is **converted** into analog **format** so that it can be, viewed on an oscilloscope. In this manner, signals which are...1 Overview
The development system provides an interactive design environment to create processing subsystems in **graphical** form, as signal flow diagrams, and implement those subsystems easily and efficiently on the SPROC...

...design is then preferably placed by the designer in a signal flow diagram (using a **graphic** user interface). Parameters for the various blocks of the design are defined by the designer...

...and parameters are automatically converted into code by the software. The development system's SPROCview **graphical** design interface enables a simple **graphical** approach to design capture. Capturing the design consists of entering the design as a signal...must be generated to run on the chip. The SPROCbuild utility completes this for the **designer** by automatically **converting** the diagram and data files into code, scheduling and linking the code, and generating a...

...optimize the design, the designer can modify the values of data and observe the corresponding **changes** in **design** performance. If the development system is connected to a signal generator, one can simulate various input signals and evaluate how the **design** reacts. **Changes** made to **design** parameters using SDI are temporary. The designer must modify the, schematic diagram and/or definition...shell controls function calls among development system software components and provides a means for the **designer** to **change** certain system defaults. The SPROCview **graphical design** interface provides for easy creation of signal flow block diagrams by supporting the import of...

...configuration supports version 4.04 of OrCAD software and its schematic capture tool, Draft. The **graphical** design interface includes the library structure required to use the SPROCcells function library with OrCAD...

...filter design interface creates the custom code and definition data for filter cells placed in **designs** during diagram entry. The SPROCbuild utility **converts** signal flow block diagrams and their associated data files into the configuration file necessary to...function library contains over fifty pre-defined functions which can be used through the **graphical** interface of the SPROCtab development system. Some cells have predefined trigger keys that aid in...

...form and subroutine form (where applicable) of each cell. Each cell is represented in the **graphical** display as an icon. Other examples of cell icons can be seen in Figure I...

...y*sin) + j (x*sin + y*cos)

Terminals:

```
pin 1: i 0 <-- output < 2.0 ( fixed point format )
pin 2: q 0 <-- output < 2.0 ( fixed point format )
pin 3: x 0 <-- input < 2.0 ( fixed point format )
pin 4: y 0 <= input < 2.0 ( fixed point format )
pin 5: cos 0 <= input < 2.0 ( fixed point format )
```

...code and then into a SPROC chip configuration file and a symbol file for the **design**. The development system shell begins the **conversion** process by issuing an invocation command to the MakeSDL module. When that module is complete...chip and the data associated with that program. The load file represents the signal processing **design** specified by the **designer** using the **graphical design** interface and filter and transfer function definitions, all packaged in a format that can be...a flow diagram of the SPROC and microprocessor development environment is seen. At 2010, using **graphic** entry packages such as "Drafe", "Annotate". "ERC" and "Netlist" which are available from OrCad in...nn'croprocessor compilers and this permits the microprocessor to read or write that parameter. Where **graphic** entry and automatic programming are used for both the microprocessor and SPROC, some means for...code into code intended for the SPROC and code intended for the microprocessor. Regardless, where **graphic** entry for both signal processing and logic processing is permitted, the **graphic** entry eventually results in separate automatic compilation for both th

e SPROC and the microprocessor...cells 2256 and 2258 when filled. With the block diagram so provided on an OrCad **graphic** interface, and in accord with the above description, after translation by the MakeSDL file, the...of a high level circuit in a silicon chip from simply a sketch on a **graphic** user interface has been described for at least the real time signal processor, it will be appreciated that the text editor could be used to replace the **graphic** entry, and that while the system would not be as convenient, the **graphic** entry is not absolutely required. Similarly, the text editor could be eliminated and the system could work only from the **graphic** entry interface. Other readily evident changes include: an expanded or a different cell library; different **graphic** user interfaces; the provision of a scheduler/compiler for the SPROC which is directly compatible with the **graphic** user interface (rather than using a translator such as MakeSDL); and the provision of different ...

7/3,K/17 (Item 7 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2005 WIPO/Univentio. All rts. reserv.

00122665

A METHOD AND APPARATUS FOR TEXTURE GENERATION
PROCEDE ET DISPOSITIF DE GENERATION DE TEXTURE

Patent Applicant/Assignee:

THE SINGER COMPANY,

Inventor(s):

YAN Johnson K,

CHEN Lish-Yann,

SZABO Nicholas S,

Patent and Priority Information (Country, Number, Date):

Patent: WO 8500913 A1 19850228

Application: WO 84US1153 19840723 (PCT/WO US8401153)

Priority Application: US 83602 19830802

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AU DE FR GB JP SE

Publication Language: English

Fulltext Word Count: 14262

Fulltext Availability:

Detailed Description

Detailed Description

... multiplying devices for adding the two (6-bit) exponents.

Combined floating point to fixed point **converter** and adder 124 **converts** from floating **point format** to **fixed point format** the respective value developed by each of multipliers 120 and 122 and adds thereto the...

...one of the pilot's eye position coordinates X_0 , Y_0 or Z_0 , generated by digital **image** generator 102, to develop earth coordinate values defined by the appropriate two of the three...

?

9/3,K/1 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00893130

Method and apparatus implementing high resolution rendition of Z-buffered
primitives

Verfahren und Gerat zur Wiedergabe von Z-gepufferten Primitiven mit hoher
Auflösung

Methode et appareil avec reproduction haute-resolution de primitives dans
un tampon Z

PATENT ASSIGNEE:

Sun Microsystems, Inc., (2616588), 901 San Antonio Road, Palo Alto,
California 94303, (US), (Proprietor designated states: all)

INVENTOR:

Deering, Michael F., 657 Cuesta Drive, Los Altos, CA 94024, (US)

LEGAL REPRESENTATIVE:

Zangs, Rainer E., Dipl.-Ing. et al (72561), Hoffmann Eitle, Patent- und
Rechtsanwalte, Arabellastrasse 4, 81925 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 817126 A2 980107 (Basic)
EP 817126 A3 980603
EP 817126 B1 021218

APPLICATION (CC, No, Date): EP 97110786 970701;

PRIORITY (CC, No, Date): US 673117 960701

DESIGNATED STATES: DE; FR; GB; IT; NL; SE

INTERNATIONAL PATENT CLASS: G06T-015/10

ABSTRACT WORD COUNT: 162

NOTE:

Figure number on first page: 3

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	199802	813
CLAIMS B	(English)	200251	1079
CLAIMS B	(German)	200251	949
CLAIMS B	(French)	200251	1219
SPEC A	(English)	199802	4052
SPEC B	(English)	200251	4157
Total word count - document A			4866
Total word count - document B			7404
Total word count - documents A + B			12270

...CLAIMS said largest vertex value from with said values of said vertices
associated with said primitive;
converting to fixed point format a result of said subtracting;
performing at least one of (i...
...vertex value from with said values of said vertices associated with said
primitive;
a floating point to fixed point format converter coupled to
convert to fixed point format said largest vertex value;
a video set-up and/or conversion video display unit;
a...
...point to fixed point converted coupled to an output of said video
display unit that converts to floating point format final
Z-values associated with each pixel to be displayed for each of said
objects...

?

12/3,K/1 (Item 1 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

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07706357 E.I. No: EIP05469473929

Title: A customisable system for real-time image processing using the Blackfin DSPprocessor and the MicroC/OS-II real-time kernel

Author: Coffey, Stephen; Connell, Joseph

Corporate Source: Electronic Engineering Dept. Cork Institute of Technology, Bishopstown, Cork, Ireland

Conference Title: Opto-Ireland 2005: Imaging and Vision

Conference Location: Dublin, Ireland Conference Date: 20050405-20050406

E.I. Conference No.: 65979

Source: Proceedings of SPIE - The International Society for Optical Engineering Opto-Ireland 2005: Imaging and Vision v 5823 2005.

Publication Year: 2005

CODEN: PSISDG ISSN: 0277-786X

Language: English

Title: A customisable system for real-time image processing using the Blackfin DSPprocessor and the MicroC/OS-II real-time kernel

Abstract: This paper presents a development platform for real-time image processing based on the ADSP-BF533 Blackfin processor and the MicroC/OS-II real-time...

...the Analog Devices/Intel Micro Signal Architecture (MSA), are a broad family of 16-bit **fixed - point** products with a dual Multiply Accumulate (MAC) core. In addition, they have a rich instruction set with **variable** instruction **length** and both DSP and MCU functionality thus making them ideal for media based applications. Using...

...proposed system can capture and process raw RGB data from any standard 8-bit greyscale **image** sensor in soft real-time and then display the processed result using a simple PC **graphical** user interface (GUI). Additionally, the GUI allows configuration of the **image** capture rate and the system and core DSP clock rates thereby allowing connectivity to a selection of **image** sensors and memory devices. The GUI also allows selection from a set of **image** processing algorithms based in the embedded operating system. 5 Refs.

Descriptors: *Image processing; Program processors; Digital signal processing; Real time systems; Computer operating systems; **Image** sensors; **Graphical** user interfaces; Data storage equipment; Embedded systems; Signal theory; Scheduling

12/3,K/2 (Item 2 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

(c) 2006 Elsevier Eng. Info. Inc. All rts. reserv.

06827239 E.I. No: EIP04188142124

Title: Fast multiplication-free QWDCT for DV coding standard

Author: Silva, Antonio; Gouveia, Paulo; Navarro, Antonio

Corporate Source: Telecommunications Institute University of Aveiro, 3810-Aveiro, Portugal

Source: IEEE Transactions on Consumer Electronics v 50 n 1 February 2004. p 180-187

Publication Year: 2004

CODEN: ITCEDA ISSN: 0098-3063

Language: English

...Abstract: comparison to a floating-point implementation. Our solution

11/3,K/1 (Item 1 from file: 34)
DIALOG(R)File 34:SciSearch(R) Cited Ref Sci
(c) 2006 Inst for Sci Info. All rts. reserv.

03985369 Genuine Article#: QX271 No. References: 33
Title: FAST COMBINATORIAL RNS PROCESSORS FOR DSP APPLICATIONS
Author(s): DICLAUDIO ED; PIAZZA F; ORLANDI G
Corporate Source: UNIV ROMA LA SAPIENZA,DEPT INFORMAT & COMMUN,VAI
EUDOSSIANA 18/I-00184 ROME//ITALY/; UNIV ANCONA,DEPT ELECTR &
AUTOMAT/I-60100 ANCONA//ITALY/
Journal: IEEE TRANSACTIONS ON COMPUTERS, 1995, V44, N5 (MAY), P624-633
ISSN: 0018-9340
Language: ENGLISH Document Type: ARTICLE (Abstract Available)

Abstract: It is known that RNS VLSI processors can parallelize **fixed - point** addition and multiplication operations by the use of the Chinese Remainder Theorem (CRT). The required...
...Identifiers--MODULAR MULTIPLICATION; RESIDUE; **CONVERTER; DESIGN**
...Research Fronts: BFSK WAVE-FORMS IN PARTIAL-BAND NOISE JAMMING ENVIRONMENT)
93-0394 002 (VECTOR QUANTIZATION; DIGITAL **IMAGE** COMPRESSION TECHNIQUES; CODING GAIN; **VARIABLE - LENGTH** SCALAR QUANTIZER)
93-2710 002 (RESIDUE NUMBER-SYSTEMS; FAST ALGORITHMS; DIGITAL SIGNAL-PROCESSING; INTEGER ARITHMETIC)

11/3,K/2 (Item 1 from file: 144)
DIALOG(R)File 144:Pascal
(c) 2005 INIST/CNRS. All rts. reserv.

16115751 PASCAL No.: 03-0274375
A variable -radix digit -serial design methodology and its application to the discrete cosine transform
LEONG M P; LEONG Philip H W
Department of Computer Science and Engineering, The Chinese University of Hong Kong, Shatin, N.T., Hong Kong
Journal: IEEE transactions on very large scale integration (VLSI) systems , 2003, 11 (1) 90-104
Language: English

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A variable -radix digit -serial design methodology and its application to the discrete cosine transform

A **variable -radix digit** -serial design methodology and its application to the implementation of a systolic **structure** for computing the discrete cosine **transform** is presented. Based on the parameters supplied by a user, different **fixed - point** designs can be derived from a single floating-point description where tradeoffs among quantization effects...

English Descriptors: Circuit **design** ; Implementation; Field programmable gate array; Discrete cosine **transforms** ; **Fixed point** ; Image processing

French Descriptors: Conception circuit; Implementation; Reseau porte programmable; Transformation cosinus discrete; Virgule fixe; Traitement **image**

Spanish Descriptors: Diseno circuito; Ejecucion; Red puerta programable; Coma fija; Procesamiento **imagen**

File 9:Business & Industry(R) Jul/1994-2006/Jan 06
(c) 2006 The Gale Group
File 15:ABI/Inform(R) 1971-2006/Jan 06
(c) 2006 ProQuest Info&Learning
File 16:Gale Group PROMT(R) 1990-2006/Jan 06
(c) 2006 The Gale Group
File 20:Dialog Global Reporter 1997-2006/Jan 06
(c) 2006 Dialog
File 47:Gale Group Magazine DB(TM) 1959-2006/Jan 06
(c) 2006 The Gale group
File 75:TGG Management Contents(R) 86-2006/Dec W4
(c) 2006 The Gale Group
File 80:TGG Aerospace/Def.Mkts(R) 1982-2006/Jan 06
(c) 2006 The Gale Group
File 88:Gale Group Business A.R.T.S. 1976-2006/Jan 04
(c) 2006 The Gale Group
File 98:General Sci Abs/Full-Text 1984-2004/Dec
(c) 2005 The HW Wilson Co.
File 112:UBM Industry News 1998-2004/Jan 27
(c) 2004 United Business Media
File 141:Readers Guide 1983-2004/Dec
(c) 2005 The HW Wilson Co
File 148:Gale Group Trade & Industry DB 1976-2006/Jan 06
(c)2006 The Gale Group
File 160:Gale Group PROMT(R) 1972-1989
(c) 1999 The Gale Group
File 275:Gale Group Computer DB(TM) 1983-2006/Jan 06
(c) 2006 The Gale Group
File 264:DIALOG Defense Newsletters 1989-2006/Jan 05
(c) 2006 Dialog
File 369:New Scientist 1994-2005/Aug W2
(c) 2005 Reed Business Information Ltd.
File 370:Science 1996-1999/Jul W3
(c) 1999 AAAS
File 484:Periodical Abs Plustext 1986-2006/Jan W1
(c) 2006 ProQuest
File 553:Wilson Bus. Abs. FullText 1982-2004/Dec
(c) 2005 The HW Wilson Co
File 570:Gale Group MARS(R) 1984-2006/Jan 06
(c) 2006 The Gale Group
File 608:KR/T Bus.News. 1992-2006/Jan 06
(c)2006 Knight Ridder/Tribune Bus News
File 620:EIU:Viewswire 2005/Oct 19
(c) 2005 Economist Intelligence Unit
File 613:PR Newswire 1999-2006/Jan 06
(c) 2006 PR Newswire Association Inc
File 621:Gale Group New Prod.Annou.(R) 1985-2006/Jan 06
(c) 2006 The Gale Group
File 623:Business Week 1985-2005/Dec 22
(c) 2005 The McGraw-Hill Companies Inc
File 624:McGraw-Hill Publications 1985-2006/Jan 06
(c) 2006 McGraw-Hill Co. Inc
File 634:San Jose Mercury Jun 1985-2006/Jan 05
(c) 2006 San Jose Mercury News
File 635:Business Dateline(R) 1985-2006/Jan 06
(c) 2006 ProQuest Info&Learning
File 636:Gale Group Newsletter DB(TM) 1987-2006/Jan 05
(c) 2006 The Gale Group
File 647:CMP Computer Fulltext 1988-2006/Jan W2
(c) 2006 CMP Media, LLC
File 696:DIALOG Telecom. Newsletters 1995-2006/Jan 05

(c) 2006 Dialog
 File 674:Computer News Fulltext 1989-2005/Oct W2
 (c) 2005 IDG Communications
 File 810:Business Wire 1986-1999/Feb 28
 (c) 1999 Business Wire
 File 813:PR Newswire 1987-1999/Apr 30
 (c) 1999 PR Newswire Association Inc
 File 587:Jane`s Defense&Aerospace 2005/Dec W3
 (c) 2005 Jane`s Information Group

Set	Items	Description
S1	1223660	(CONVERT????? OR CONVER??????? OR TRANSFORM????? OR CHANG?- ??? OR TRANSLAT????) (7N) (FORMAT?? OR DESIGN?? OR STRUCTU???? - OR ARRANGE????? OR PATTERN???)
S2	26652	FIX?? (2N) POINT??
S3	1663	S2 (7N) (FORMAT?? OR DESIGN?? OR STRUCTU???? OR ARRANGE????? OR PATTERN???)
S4	8509008	IMAG?? OR GRAPHIC??? OR PICTURE??
S5	379933	(RENDER???? OR PROCESS????) (5N) S4
S6	2075	AU=(WANG L? OR WANG, L? OR DENG K? OR DENG, K? OR GUO B? OR GUO, B? OR BUCKMAN J? OR BUCKMAN, J?)
S7	2	S1(S) S3(S) S4
S8	21	S1(35N) S3(35N) S4
S9	10	RD (unique items)
S10	8	S9 NOT S7
S11	0	S6 AND S1 AND S2

7/3,K/1 (Item 1 from file: 160)
DIALOG(R)File 160:Gale Group PROMT(R)
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01764245

HIGH RESOLUTION GRAPHICS SYSTEM FOR DISPLAY OF PROCESS AND PRODUCTION APPLICATIONS

News Release August 21, 1987 p. 1

A high-resolution **graphics** system for SIMATIC programmable controllers is being introduced by Siemens. Designated "CP 526," the system ...

... monitoring and communication across a broad range of production and process automation applications. Text, color **graphics**, semi- **graphics** (individual symbols) and bars can be used to represent a particular process. As a built-in function of the software, colors and symbols can flash or change to enhance the **picture** or to alert the operator to an emergency condition or a change in the process...

... foreground and eight background colors are available, giving the user the ability to design enhanced **pictures**. Process data can be presented in the following **formats**: 16-bit BCD; 16-bit **fixed - point**; 32-bit floating-point; time; count; and date/time. Since this can be accomplished automatically via the system's internal software, users save programming and debugging time when **converting** to a desired **format**. PLC memory is also conserved.

Full text available on PTS New Product Announcements.

...

7/3,K/2 (Item 1 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2006 The Gale Group. All rts. reserv.

01242817 SUPPLIER NUMBER: 06222850 (USE FORMAT 7 OR 9 FOR FULL TEXT)
A potpourri of DSP. (digital signal processing) (IC Special)

Bond, John

ESD: The Electronic System Design Magazine, v18, n1, p65(8)

Jan, 1988

ISSN: 0893-2565 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 3907 LINE COUNT: 00304

... extended to 16 Mbytes. In both cases the memory is completely byte addressable. So for **image** processing, pixel values can be stored a byte at a time without wasting an entire 32-bit location. The 32C has single-cycle data **format conversions** directly to and from IEEE databases.

Fixed - Point Device

Another processor that has proven useful for image processing comes from Analog Devices (Norwood...

10/3,K/1 (Item 1 from file: 9)
DIALOG(R)File 9:Business & Industry(R)
(c) 2006 The Gale Group. All rts. reserv.

03462188 Supplier Number: 123201494 (USE FORMAT 7 OR 9 FOR FULLTEXT)
Tool targets DSP developers using Matlab.
(Leading edge: what's hot in the design community)
EDN, v 49, n 20, p 20
September 30, 2004
DOCUMENT TYPE: Journal ISSN: 0012-7515 (United States)
LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 297

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:

...supports common processor-specific operations, such as multiply with shift and saturate. Type propagation simplifies **conversion** of **design** to **fixed - point** versions. With this feature, operations using variables that you define as fixed-point types automatically...

...www.analog.com) in processor-specific versions of the tool.

The Analyzer provides an interactive **graphical** user interface for analysis and debugging of floating-to-fixed-point conversions and displays the...

10/3,K/2 (Item 1 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
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11607984 Supplier Number: 124083771 (USE FORMAT 7 FOR FULLTEXT)
AccelChip Executive Advocates Algorithmic Synthesis at Multiple IEEE Meetings.
Business Wire, pNA
Nov 4, 2004
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 479

... to go rapidly from concept to silicon."

AccelChip's architectural synthesis graphically assists floating to **fixed - point conversion** and provides **design** and device-specific scheduling and optimization.

Bohm's presentation focuses on the process and challenges...

...challenges of using FPGAs as DSP accelerators.

Formerly Chief Scientist and Technology Fellow at Mentor **Graphics** , Michael Bohm currently oversees all product development for AccelChip Inc. He also supervised the development...

10/3,K/3 (Item 2 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
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11505766 Supplier Number: 122728308 (USE FORMAT 7 FOR FULLTEXT)
AccelChip Inc. Improves Quality of Results and Furthers Integration with Key Industry Partners.

Business Wire, pNA
Sept 27, 2004
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 804

... the new release, we are targeting advanced array-based designs, such as those found in **image** processing, radar, sonar, and real time communication, with new functionality that on specific designs decreases...

...MATLAB source -- increasing efficiency and providing unheard of flexibility. The tool's enhanced floating- to **fixed - point conversion** and enhanced scheduler provide the **designer** much better control over trade-offs between throughput, performance, area, and accuracy.

By reducing the...

10/3,K/4 (Item 3 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
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11505672 Supplier Number: 122728210 (USE FORMAT 7 FOR FULLTEXT)
Catalytic Inc. Reduces DSP Development Times With DSP Design Automation Software; Fixed-Point DSP Studio To Be Demonstrated This Week During GSPx.

Business Wire, pNA
Sept 27, 2004
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 775

... within the MATLAB environment using the DSP Studio simulation acceleration and analysis capabilities.

The Catalytic **Fixed - Point** Studio is **designed** for DSP development teams who employ MATLAB as an algorithm development environment and who implement...

...and supports common processor-specific operations, such as multiply with shift and saturate. Conversion of **designs** to **fixed - point** versions is simplified by type propagation -- operations using variables defined as fixed-point types automatically...

...all variables. The Analyzer's call-graph and error reporting facilitate easy traversal of a **design**, debugging and management of the **conversion** process.

The DSP Studio Accelerator decreases simulation times of floating-point and fixed-point MATLAB...

...optimize simulation performance, as well as provide rapid analysis and feedback on the status of **fixed - point design conversion**. It has applied for several patents on its technology.

Over the next six months, Catalytic Inc. will unveil additional software to add to its DSP **Design** Automation environment.

Pricing and Availability

The **Fixed - Point** DSP Studio begins shipping in Q4 2004 on Windows operating systems. Pricing starts at \$4...

10/3,K/5 (Item 1 from file: 20)
DIALOG(R)File 20:Dialog Global Reporter
(c) 2006 Dialog. All rts. reserv.

44376574 (USE FORMAT 7 OR 9 FOR FULLTEXT)

**Genesis Microchip Selects Catalytic RMS to Streamline Algorithm Development
for Image Processing Systems**

BUSINESS WIRE

September 07, 2005

JOURNAL CODE: WBWE LANGUAGE: English RECORD TYPE: FULLTEXT

WORD COUNT: 294

... time, today announced that Genesis(R) Microchip Inc., a world leader in the development of **image** processing technologies for flat-panel monitors, TVs and other consumer display products, has selected Catalytic RMS to simplify the design flow for its **image** processing solutions.

Catalytic RMS (Rapid MATLAB(R) Simulator) works seamlessly with MATLAB from The MathWorks...

... provide fast floating-point simulation for MATLAB code, accurate fixed-point modeling capabilities, and fast **fixed - point** simulation. The result is a streamlined algorithm **design** process that accelerates MATLAB simulation and helps **convert** floating-point MATLAB to optimized fixed-point MATLAB, which accurately models the behavior of the...

10/3,K/6 (Item 2 from file: 20)

DIALOG(R)File 20:Dialog Global Reporter

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16097023 (USE FORMAT 7 OR 9 FOR FULLTEXT)

**EDN Magazine Announces Electronics Industry Innovation/Innovator of the
Year Awards Winners**

BUSINESS WIRE

April 10, 2001

JOURNAL CODE: WBWE LANGUAGE: English RECORD TYPE: FULLTEXT

WORD COUNT: 522

(USE FORMAT 7 OR 9 FOR FULLTEXT)

... Transceiver

Digital ICs

Analog Devices, AD1896 Asynchronous Sample Rate Converter

Multimedia Functions

Nvidia, GeForce2 3D **Graphics** Architecture and Family of Processors

Peripherals

Think Outside, Stowaway Portable Keyboard

Power Sources and Controllers

SynQor, PowerQor Quarter-Brick dc/dc **Converter**

Software

The Mathworks, Filter **Design** Toolbox 2.0

Test & Measurement

Tektronix, TDS7000 Series Digital-Phosphor Oscilloscopes

Microprocessors/Microcontrollers

Infineon, TC1775 32-Bit Processor

Digital-Signal Processors

Texas Instruments, TMS320C55x **Fixed - Point** DSP

EDN, the leading **design** magazine of the electronics OEM, is published by Cahners Business Information. Headquartered at 275 Washington

...

10/3,K/7 (Item 1 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2006 The Gale Group. All rts. reserv.

0017497810 SUPPLIER NUMBER: 123201494 (USE FORMAT 7 OR 9 FOR FULL
TEXT)

**Tool targets DSP developers using Matlab.(Leading edge: what's hot in the
design community)**

Moretti, Gabe

EDN, 49, 20, 20(1)

Sept 30, 2004

ISSN: 0012-7515 LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 349 LINE COUNT: 00032

... supports common processor-specific operations, such as multiply
with shift and saturate. Type propagation simplifies **conversion** of
design to **fixed - point** versions. With this feature, operations using
variables that you define as fixed-point types automatically...

...www.analog.com) in processor-specific versions of the tool.

The Analyzer provides an interactive **graphical** user interface for
analysis and debugging of floating-to-fixed-point conversions and displays
the...

10/3,K/8 (Item 1 from file: 621)
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)
(c) 2006 The Gale Group. All rts. reserv.

01066464 Supplier Number: 40315188 (USE FORMAT 7 FOR FULLTEXT)
**GRAPHICS SUPERCOMPUTER FROM ARDENT PERFORMS 64 MILLION FLOATING-POINT
OPERATIONS, 200 THOUSAND SHADED 3D POLYGONS PER SECOND**

News Release, p1

March 1, 1988

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Trade

Word Count: 912

... be vectorized or
parallelized.

The Titan architecture is designed to apply supercomputer performance
directly to **graphics** computation. Each integer unit, in addition to
executing application programs. acts as a display list processor for
graphics code. Integer units also format commands and pass them to
the parallel pixel and polygon...

...special-purpose microcoded circuitry. These include transforming
coordinates, perspective division, backface rejection, clipping,
shading and **conversion** to the **fixed point formats**
required by the
parallel pixel and polygon processors.

Ardent's Dynamic Object-Rendering Environment (Dore...

...part of Titan. The Dore'
software library lets users describe a scene, produce highly complex
images from scene data, and manipulate the massive amounts of data
interactively and dynamically.

· · · Dore' uses...
?

is suitable to be programmed into any **fixed - point** arithmetic processor decreasing the consumer equipment cost. This solution is still compatible with the standard...

Descriptors: ***Image** coding; Cosine transforms; Television standards; Digital arithmetic; Computational complexity; **Image** compression; Algorithms; Video recording; **Image** quality; Redundancy; Polynomials; Video cameras; Computer software

Identifiers: Digital video; Discrete cosine transform; **Fixed - point** processing; **Variable length** coding

12/3,K/3 (Item 1 from file: 34)

DIALOG(R)File 34:SciSearch(R) Cited Ref Sci
(c) 2006 Inst for Sci Info. All rts. reserv.

04305105 Genuine Article#: RU967 No. References: 22

Title: CYCLOSTATIONARY MODELING, ANALYSIS, AND OPTIMAL COMPENSATION OF QUANTIZATION ERRORS IN SUBBAND CODECS

Author(s): UZUN N; HADDAD RA

Corporate Source: POLYTECH INST NEW YORK, DEPT ELECT ENGN, 6 METROTECH CTR/BROOKLYN//NY/11201; BELLCORE, MULTIPLEX & MULTIACCESS TECHNOL GRP/RED BANK//NJ/00000

Journal: IEEE TRANSACTIONS ON SIGNAL PROCESSING, 1995, V43, N9 (SEP), P 2109-2119

ISSN: 1053-587X

Language: ENGLISH Document Type: ARTICLE (Abstract Available)

...Abstract: quantizer wherein only the mantissa is uniformly quantized is also analyzed and compared with the **fixed point**, pdf-optimized filter bank. For high bit rates, their performance is comparable.

Research Fronts: 93-0394 002 (VECTOR QUANTIZATION; DIGITAL **IMAGE** COMPRESSION TECHNIQUES; CODING GAIN; **VARIABLE - LENGTH** SCALAR QUANTIZER)

12/3,K/4 (Item 2 from file: 34)

DIALOG(R)File 34:SciSearch(R) Cited Ref Sci
(c) 2006 Inst for Sci Info. All rts. reserv.

03996304 Genuine Article#: QX758 No. References: 40

Title: NEW SYSTOLIC ARRAY IMPLEMENTATION OF THE 2-D DISCRETE COSINE TRANSFORM AND ITS INVERSE

Author(s): CHANG YT; WANG CL

Corporate Source: NATL TSING HUA UNIV, INST ELECT ENGN/HSINCHU 30043//TAIWAN/

Journal: IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO TECHNOLOGY, 1995, V5, N2 (APR), P150-157

ISSN: 1051-8215

Language: ENGLISH Document Type: ARTICLE (Abstract Available)

...Abstract: IDCT). Simulation results demonstrate that the proposed 2-D DCT and IDCT architectures have good **fixed - point** error performance for both real **image** and random data, As a consequence, they are useful for applications where very high throughput...

...Research Fronts: 003 (VIDEO SYSTEMS; PARALLEL ARCHITECTURES; MULTIMEDIA NETWORK DISTRIBUTION CENTER)

93-0394 001 (VECTOR QUANTIZATION; DIGITAL **IMAGE** COMPRESSION TECHNIQUES; CODING GAIN; **VARIABLE - LENGTH** SCALAR QUANTIZER)

12/3,K/5 (Item 1 from file: 144)
DIALOG(R)File 144:Pascal
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16537534 PASCAL No.: 04-0185139
Fast multiplication-free QWDCT for DV coding standard
SILVA A; GOUVEIA P; NAVARRO A
Telecommunications Institute University of Aveiro, 3810-Aveiro, Portugal
Journal: IEEE Transactions on Consumer Electronics, 2004, 50 (1) 180-187
Language: English

... comparison to a floating-point implementation. Our solution is suitable to be programmed into any **fixed - point** arithmetic processor decreasing the consumer equipment cost. This solution is still compatible with the standard...

English Descriptors: Digital video; Discrete cosine transform; **Fixed - point** processing; **Variable length** coding; Application; Cosine transforms; Television standards; Digital arithmetic; Computational complexity; **Image** compression; Algorithms; Video recording; **Image** quality; Redundancy; Polynomials; Video cameras; Computer software; **Image** coding; Theory; Experiments

French Descriptors: Application; Transformation cosinus; Norme television; Arithmetique numerique; Complexite calcul; Compression **image** ; Algorithmes; Enregistrement video; Qualite **image** ; Redondance; Polynome; Camera television; Logiciel; Codage **image** ; Theorie; Experience

12/3,K/6 (Item 1 from file: 239)
DIALOG(R)File 239:Mathsci
(c) 2005 American Mathematical Society. All rts. reserv.

03595118 MR 2004k#11028
Automatic sequences.
Theory, applications, generalizations.
Allouche, Jean-Paul
Shallit, Jeffrey (Department of Computer Science, University of Waterloo, Waterloo, Ontario, N2L 3G1, Canada)
Corporate Source Codes: 3-WTRL-C
Publ: Cambridge University Press, Cambridge,
2003, xvi+571 pp. ISBN: 0-521-82332-3
Language: English
Subfile: MR (Mathematical Reviews) AMS
Abstract Length: LONG (104 lines)
Reviewer: Berthe, Valerie (Montpellier)

...sequences, and sequences that are approximated by automatic sequences. Applications to number theory, physics, computer **graphics** , and even music are also evoked.

This book is meant for graduate or advanced undergraduate...

...simple models of computation, which leads to the introduction of automatic sequences in Chapter 5. **Fixed points** of uniform morphisms of the free monoid are then studied in Chapter 6; these are...

...Cobham) is that a sequence is k -automatic if and only if it is the **image** under a coding of a **fixed point** of a k -uniform morphism. General morphisms are also considered in Chapter 7, a celebrated example of a **fixed point** of a non-uniform morphism being the Fibonacci word. It

is proved in particular that the **image** of any morphic sequence by any morphism is either finite or morphic (this theorem is...

Descriptors: ...; 94A45 -Information and communication, circuits-Communication, information-Prefix, **length** - **variable** , comma-free codes (See also 20M35, 68Q45)

12/3,K/7 (Item 2 from file: 239)
DIALOG(R)File 239:Mathsci
(c) 2005 American Mathematical Society. All rts. reserv.

01679632 MR 82i#58052
Period doubling bifurcations for families of maps on \mathbb{R}^n .
Collet, P.
Eckmann, J.-P.
Koch, H.
(Koch, Hans)
J. Statist. Phys.
Journal of Statistical Physics, 1981, 25, no. 1, 1--14. ISSN:
0022-4715 CODEN: JSTPBS
Language: English
Subfile: MR (Mathematical Reviews) AMS
Abstract Length: LONG (54 lines)
Reviewer: Nitecki, Zbigniew (Medford, Mass.)

... $\Lambda(f)$, where $\Lambda(f)$ is a coordinate transformation, such that T has a saddle **fixed point** $f_{\text{sub}}0$ (in an appropriate space of maps) with one-dimensional unstable manifold. The **fixed point** $f_{\text{sub}}0$ is a self-similar map ($f_{\text{sub}}0$ is conjugate to f ...

...of maps with a flip bifurcation transversally, then successive period-doublings will correspond to successive **images** of this bifurcation submanifold under T . By standard hyperbolicity arguments, these **images** will cross the arc $\phi_{\text{sub}}t$ at points which accumulate on $W^{\text{sup}}u$...

Descriptors: ; 26B15 -Real functions (See also 54C30)-Functions of several **variables** -Integration: **length** , area, volume (See also 28A75, 51M25...

?

16/3,K/1 (Item 1 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

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07706357 E.I. No: EIP05469473929

Title: A customisable system for real-time image processing using the Blackfin DSPprocessor and the MicroC/OS-II real-time kernel

Author: Coffey, Stephen; Connell, Joseph

Corporate Source: Electronic Engineering Dept. Cork Institute of Technology, Bishopstown, Cork, Ireland

Conference Title: Opto-Ireland 2005: Imaging and Vision

Conference Location: Dublin, Ireland Conference Date: 20050405-20050406

E.I. Conference No.: 65979

Source: Proceedings of SPIE - The International Society for Optical Engineering Opto-Ireland 2005: Imaging and Vision v 5823 2005.

Publication Year: 2005

CODEN: PSISDG ISSN: 0277-786X

Language: English

Title: A customisable system for real-time image processing using the Blackfin DSPprocessor and the MicroC/OS-II real-time kernel

Abstract: This paper presents a development platform for real-time **image processing** based on the ADSP-BF533 Blackfin processor and the MicroC/OS-II real-time operating...

...the Analog Devices/Intel Micro Signal Architecture (MSA), are a broad family of 16-bit **fixed - point** products with a dual Multiply Accumulate (MAC) core. In addition, they have a rich instruction set with **variable instruction length** and both DSP and MCU functionality thus making them ideal for media based applications. Using...

...any standard 8-bit greyscale image sensor in soft real-time and then display the **processed** result using a simple PC **graphical** user interface (GUI). Additionally, the GUI allows configuration of the image capture rate and the...

...of image sensors and memory devices. The GUI also allows selection from a set of **image processing** algorithms based in the embedded operating system. 5 Refs.

Descriptors: ***Image processing** ; Program **processors** ; Digital signal **processing** ; Real time systems; Computer operating systems; Image sensors; Graphical user interfaces; Data storage equipment; Embedded...

?

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File 8:Ei Compendex(R) 1970-2006/Dec W4
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(c) 2005 The HW Wilson Co.
File 144:Pascal 1973-2005/Dec W2
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(c) 2005 American Mathematical Society
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File 248:PIRA 1975-2006/Dec W3
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Set	Items	Description
S1	28937	(VARIABL??? OR ADJUST????)(2N)(LENGTH?? OR DIGIT??)
S2	109564	FIX??(2N)POINT??
S3	3290654	IMAG?? OR GRAPHIC???? OR PICTURE??
S4	706302	(CONVERT????? OR CONVER??????? OR TRANSFORM????? OR CHANG?- ??? OR TRANSLAT????)(7N)(FORMAT?? OR DESIGN?? OR STRUCTU???? - OR ARRANGE????? OR PATTERN???)
S5	496	DIRECT3D OR DIRECT???()3D??
S6	55333	AU=(WANG L? OR WANG, L? OR DENG K? OR DENG, K? OR GUO B? OR GUO, B? OR BUCKMAN J? OR BUCKMAN, J?)
S7	611272	(RENDER????? OR PROCESS????)(5N)S3
S8	0	S1 AND S2 AND S4 AND S5 AND S7
S9	9	S1 AND S2 AND S3
S10	2	S9 AND S4
S11	2	RD (unique items)
S12	7	S9 NOT S10
S13	116	S1 AND S2
S14	0	S13 AND S5
S15	2	S13 AND S7
S16	1	S15 NOT S11

S17

0 S6 AND S1 AND S2